Synopsys Wins TSMC's 2012 "Interface IP Partner of the Year" Award

Excellent Customer Support, Technical Leadership and Number of Customer Tape-Outs Cited as Key Selection Criteria

MOUNTAIN VIEW, Calif., Oct. 17, 2012 /PRNewswire/ --

Highlights:

- Selection based on customer feedback, TSMC-9000 compliance, technical support excellence and number of customer tape-outs
- Synopsys' extensive portfolio of high-quality DesignWare® Interface IP includes USB, PCI Express, DDR, MIPI, HDMI and SATA solutions
- Silicon-proven on a wide range of TSMC low-power and high-performance processes from 180nm to 28nm
- Thoroughly characterized across process, voltage and temperature (PVT) variations in both High-K Metal Gate and PolySiON technologies to ensure design robustness

Synopsys, Inc. (Nasdaq:SNPS), a global leader providing software, IP and services used to accelerate innovation in chips and electronic systems, today announced that it received TSMC's 2012 Interface IP Partner of the Year Award for the third consecutive year. Synopsys was selected based on customer feedback, TSMC-9000 compliance, technical support excellence and number of customer tape-outs. Synopsys' DesignWare Interface IP portfolio includes widely used protocols such as USB, PCI Express, DDR, MIPI, HDMI and SATA that are offered in a broad range of processes from 180 nanometer (nm) to 28nm.

"Synopsys has consistently been working with the TSMC-9000 program to help our mutual customers achieve their design goals," said Suk Lee, senior director of design infrastructure marketing at TSMC. "This award recognizes Synopsys for their important contributions to the TSMC ecosystem and continuous dedication to providing designers with the IP needed to help them build differentiated products and get them to market quickly."

"For more than a decade, TSMC and Synopsys have built a strong relationship based on the common purpose of providing designers with a broad range of proven, easy-to-integrate IP," said John Koeter, vice president of marketing for IP and systems at Synopsys. "We are honored that TSMC has chosen us as their 2012 Interface IP Partner of the Year. To date, Synopsys has delivered more than 500 DesignWare IP products on TSMC processes from 180 to 28 nanometers, enabling designers to reduce their project risk and accelerate time-to-volume production."

About DesignWare IP
Synopsys is a leading provider of high-quality, silicon-proven IP solutions for SoC designs. The broad DesignWare IP portfolio includes complete interface IP solutions consisting of controllers, PHY and verification IP for widely used protocols, analog IP, embedded memories, logic libraries, processor cores and subsystems. To support software development and hardware/software integration of the IP, Synopsys offers drivers, transaction-level models and prototypes for many of its IP products. Synopsys' HAPS® FPGA-Based Prototyping Solution enables validation of the IP and the SoC in the system context. Synopsys' Virtualizer™ virtual prototyping tool set allows developers to start the development of software for the IP or the entire SoC significantly earlier compared to traditional methods. With a robust IP development methodology, extensive investment in quality, IP prototyping, software development and comprehensive technical support, Synopsys enables designers to accelerate time-to-market and reduce integration risk. For more information on DesignWare IP, visit http://www.synopsys.com/designware

About Synopsys
Synopsys, Inc. (Nasdaq:SNPS) accelerates innovation in the global electronics market. As a leader in electronic design automation (EDA) and semiconductor IP, its software, IP and services help engineers address their design, verification, system and manufacturing challenges. Since 1986, engineers around the world have been using Synopsys technology to design and create billions of chips and systems. Learn more at www.synopsys.com.

Editorial Contacts:

Monica Marmie
Synopsys, Inc.
650-584-2890
monical@synopsys.com

Stephen Brennan
MCA
650-968-8900 x114
sbrennan@mcapr.com

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