Media Advisory/Alert: Synopsys Demonstrates Software Development for ARM big.LITTLE Processing at DesignWest

MOUNTAIN VIEW, Calif., March 21, 2012 /PRNewswire/ -- Synopsys, Inc. (Nasdaq:SNPS), a world leader in software and IP used in the design, verification and manufacture of electronic components and systems, will be presenting its VDK Family for ARM® Cortex™ Processors software development solution at DesignWest. The VDK Family for ARM Cortex Processors contains multiple reference designs, sample software stacks and analysis and debug software tools for the Cortex-A15 MPCore processor and ARM big.LITTLE processing. Learn how software developers can use the VDKs to speed software bring-up and gain better debug control for quad-core ARM Cortex-A15 MPCore™ processor and ARM big.LITTLE™ processing-based designs.

Visit the ARM Partner Pavilion to see a demonstration or attend the joint Android Summit technical session with Synopsys and ARM at DesignWest 2012.

TOPICS TO BE COVERED:

- How to accelerate software development up to 12 months before board availability
- How to best utilize ARM big.LITTLE processing performance and energy efficiency capabilities
- How to run multicore/multicluster software debug and analysis

For more information or to register for DesignWest now, please visit: https://designwest.reg.ubmelectronics.com/2012.

WHAT: ARM Partner Pavilion and Joint Android Summit Technical Session

WHEN: March 26-29, 2012

WHERE: McEnery Convention Center, San Jose, CA

EVENT HOURS:

Android Summit Technical Session

Wednesday, March 28 - 3:15pm-4:15pm

Exhibit Hall Hours

Tuesday, March 27 - 11:30am - 7:00pm
Wednesday, March 28 - 11:30am - 5:30pm
Thursday, March 29 - 10:30am - 3:00pm

SESSION DESCRIPTION:

- **Software Development for Android on ARM big.LITTLE processing SoCs Using Virtual Prototypes**
  As devices get more and more complex, developing software for those devices becomes increasingly complex. While big.LITTLE processing offers a way to balance high performance through the use of the ARM Cortex-A15 MPCore processor with power efficiency by migrating software tasks to the ARM Cortex-A7 MPCore processor to extend battery life, these processors challenge software developers to keep up and both utilize the available compute power while being power conscious. With the right set of models, virtual prototypes offer a unique platform view to the software developer to ease the software development for these multi-core designs. They moreover provide unique capabilities to make sure that software developers correctly utilize the available Android control functions to deliver a smooth user experience while minimizing power consumption.

For more information on the technical session, please visit: www.ubmdesign.com/sessions/android

For more information on Synopsys' Virtualizer Development Kit Family, please visit: www.synopsys.com/VDK4big-LITTLE.

About Synopsys
Synopsys, Inc. (Nasdaq:SNPS) is a world leader in electronic design automation (EDA), supplying the global electronics market with the software, intellectual property (IP) and services used in semiconductor design,

verification and manufacturing. Synopsys' comprehensive, integrated portfolio of implementation, verification, IP, manufacturing and field-programmable gate array (FPGA) solutions helps address the key challenges designers and manufacturers face today, such as power and yield management, system-to-silicon verification and time-to-results. These technology-leading solutions help give Synopsys customers a competitive edge in bringing the best products to market quickly while reducing costs and schedule risk. Synopsys is headquartered in Mountain View, California, and has approximately 70 offices located throughout North America, Europe, Japan, Asia and India. Visit Synopsys online at http://www.synopsys.com/.

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