

# BiTMICRO Selects Synopsys for Chip Design Automation

MOUNTAIN VIEW, Calif., Feb. 28, 2012 /PRNewswire/ -- Synopsys, Inc. (Nasdaq: SNPS), a world leader in software and IP used in the design, verification and manufacture of electronic components and systems, today announced that BiTMICRO Networks, a manufacturer of high-end enterprise Solid State Drive (SSD) technologies, has chosen Synopsys as its electronic design automation (EDA) provider for system-on-chip (SoC) design. BiTMICRO recently announced the successful tapeout of two third-generation SSD controllers, which are the foundation of its next-generation products, using Synopsys' Galaxy™ Implementation and Discovery™ Verification Platforms to speed SSD controller development.

BiTMICRO's pioneering TALINO-DE (Translation and Linking of I/O Nodes—Device Edition) SSD controller works with its second-stage ISIP-DE (Intelligent Storage Interconnect Platform—Device Edition) controller to achieve the required performance in mission-critical applications. These ASIC controllers were designed and developed using Synopsys' Galaxy implementation and Discovery verification platforms, which include Design Compiler® RTL synthesis, PrimeTime® signoff, HSPICE® circuit simulation and VCS® functional verification tools. Using foundry-certified models and an advanced verification methodology to eliminate bugs, Synopsys' integrated tool suites helped make it possible for BiTMICRO engineers to achieve high-quality output without any delay.

"We are proud of the significant innovations our engineering team in the Philippines has achieved with the cutting-edge TALINO-DE and ISIP-DE controller designs," said Rey Bruce, CEO of BiTMICRO Networks. "Synopsys' robust portfolio of implementation and verification solutions and superior technical support were instrumental in helping us bring our next-generation SSD products to market in a timely manner. We look forward to working with Synopsys as we continue to innovate to meet our customers' needs."

"BiTMICRO adopted Synopsys' implementation and verification solutions to address their design challenges because their engineers require silicon-proven, high-quality tools for their SoC designs," said Jian-Yue Pan, vice president of Asia-Pacific sales at Synopsys. "Synopsys is also partnering with BiTMICRO's academic arm, the Bruce Institute of Technology, to support the first microchip design center in the Philippines whose aim is to help train local engineers in advanced microchip design and engineering. Synopsys is dedicated to supporting the global IC engineering community and contributing to our customers' growth as technology leaders."

## About Synopsys®

Synopsys, Inc. (Nasdaq: SNPS) is a world leader in electronic design automation (EDA), supplying the global electronics market with the software, intellectual property (IP) and services used in semiconductor design, verification and manufacturing. Synopsys' comprehensive, integrated portfolio of implementation, verification, IP, manufacturing and field-programmable gate array (FPGA) solutions helps address the key challenges designers and manufacturers face today, such as power and yield management, system-to-silicon verification and time-to-results. These technology-leading solutions help give Synopsys customers a competitive edge in bringing the best products to market quickly while reducing costs and schedule risk. Synopsys is headquartered in Mountain View, California, and has approximately 70 offices located throughout North America, Europe, Japan, Asia and India. Visit Synopsys online at <http://www.synopsys.com/>.

## Editorial Contacts:

Sheryl Gulizia  
Synopsys, Inc.  
650-584-8635  
[sgulizia@synopsys.com](mailto:sgulizia@synopsys.com)

Lisa Gillette-Martin  
MCA, Inc.  
650-968-8900 ext. 115  
[lgmartin@mcapr.com](mailto:lgmartin@mcapr.com)

SOURCE Synopsys, Inc.

---