CSR Selects Synopsys for Advanced-Node SoC Design

Adoption of Synopsys Galaxy Platform Driven by Superior Results for ARM CPU-based SoCs

MOUNTAIN VIEW, Calif, Feb. 9, 2012 /PRNewswire/ -- Synopsys, Inc. (Nasdaq: SNPS), a world leader in software and IP used in the design, verification and manufacture of electronic components and systems, today announced that CSR plc, a leader in wireless, location and audiovisual technology, has deployed Synopsys' Galaxy™ Implementation Platform for the design of its 40-nanometer (nm) system-on-chips (SoCs). CSR cited the Galaxy platform's ability to deliver a robust hierarchical SoC design flow while converging on aggressive timing, area, and power goals as a key advantage of collaborating with Synopsys. CSR design teams are globally distributed, with key SoC groups in Cambridge, UK; Haifa, Israel; Shanghai, China and Phoenix, Arizona. Synopsys' highly responsive global support and expert consultants in efficient flows for ARM® CPU-based design were instrumental in CSR's decision to select Synopsys.

"We maintain our leadership in digital cameras, automotive navigation processors and other consumer markets by delivering innovative SoCs to our customers on time," said Babak Bastani, vice president of global chip design at CSR. "By using the silicon-proven Galaxy Platform, we are able to predictably tape out differentiated designs that deliver superior performance with low power consumption, which is critical to our success in these mobile computing markets."

CSR successfully adopted the Galaxy platform for its 40-nm, high-end Coach14 digital camera chip. This very complex SoC has millions of instances and intellectual property (IP) blocks, including Synopsys' DesignWare® USB 2.0 and DDR IP. Because logic synthesis, physical implementation and signoff are all tightly integrated in the Galaxy platform, CSR was able to deploy a hierarchical flow from synthesis to place-and-route to signoff and achieve on-time tapeout while meeting all of its design specifications.

Key components of the Galaxy platform include:

- Design Compiler® Graphical with IC Compiler: Provides faster RTL-to-physical convergence from initial design exploration through concurrent multi-corner/multi-mode (MCMM) optimization, and closure for timing, power, testability and area;
- IC Compiler Zroute technology: Offers concurrent design-for-manufacturability (DFM) routing for advanced process technologies. Coupled with In-Design physical verification via IC Validator, IC Compiler enables fast multicore, lithography-aware routing and delivers full compliance with complex DRC rules required for advanced silicon nodes; and
- PrimeTime® HyperScale technology: Speeds block-level timing closure in the context of the top-level design, dramatically accelerating signoff of complex, hierarchical designs.

"Our collaboration with CSR demonstrates that the Galaxy platform delivers the results needed at advanced nodes so leading design teams can predictably and successfully bring new products to market," said Antun Domic, senior vice president and general manager of Synopsys' Implementation Group. "With each new process geometry, design teams face many new challenges. The Galaxy Implementation Platform provides an integrated solution from RTL to GDSII so engineers can tape out designs that meet their aggressive specifications with greater confidence."

About CSR

CSR is a global provider of innovative silicon and software solutions for the location-aware, media-rich, cloud-connected world. Our platforms are optimised for the automotive navigation and **infotainment**, digital cameras and imaging, connected home infotainment and wireless audio markets. We provide solutions to complex problems in the audio-visual, connectivity and location technology domains across a broad range of markets, with a technology portfolio that includes **GPS/GNSS** systems, **Bluetooth®**, Wi-Fi®, FM, NFC, aptX® and CVC™ **audio codecs**, JPEG, MPEG, H.264 imaging, IPS printing, microcontrollers, DSPs and broadband receivers. CSR's technology solutions and market platforms enable its customers to deliver a superior user experience and are adopted by leaders in the auto, computer, home and mobile markets. More information can be found at **www.csr.com**. Keep up to date with CSR on our **blog**, or follow us on Twitter at **twitter.com/CSR_plc**.

About Synopsys®

Synopsys, Inc. (Nasdaq: SNPS) is a world leader in electronic design automation (EDA), supplying the global electronics market with the software, intellectual property (IP) and services used in semiconductor design, verification and manufacturing. Synopsys' comprehensive, integrated portfolio of implementation, verification, IP, manufacturing and field-programmable gate array (FPGA) solutions helps address the key challenges designers and manufacturers face today, such as power and yield management, system-to-silicon verification

and time-to-results. These technology-leading solutions help give Synopsys customers a competitive edge in bringing the best products to market quickly while reducing costs and schedule risk. Synopsys is headquartered in Mountain View, California, and has approximately 70 offices located throughout North America, Europe, Japan, Asia and India. Visit Synopsys online at http://www.synopsys.com/.

Editorial Contacts:

Sheryl Gulizia
Synopsys, Inc.
650-584-8635
sgulizia@synopsys.com

Lisa Gillette-Martin
MCA, Inc.
650-968-8900 ext. 115
lgmartin@mcapr.com

SOURCE Synopsys, Inc.