

Synopsys Acquires High-level Synthesis Technology from Synfora, Inc.

MOUNTAIN VIEW, Calif., June 10 /PRNewswire-FirstCall/ -- Synopsys, Inc. (Nasdaq: SNPS), a world leader in software and IP for semiconductor design, verification and manufacturing, today announced it has acquired technology, engineering resources and other assets of Synfora, Inc., a provider of C/C++ high-level synthesis tools used to design complex systems-on-chips (SoCs) and FPGAs. The asset acquisition strengthens Synopsys' position in system-level design and verification and enhances the company's FPGA-based prototyping solutions.

Synfora's technology enables designers to quickly create and synthesize IC building blocks starting from a description written in the C or C++ programming language. The advantages of Synfora's technology, including high capacity and quality of results for performance, area and power, are production-proven in leading-edge designs. Customers who have adopted Synfora's tools have experienced the benefits of the technology for their FPGA and SoC designs through integration with Synopsys' Synplify® Premier synthesis and Galaxy™ Implementation Platform.

"This acquisition adds proven C/C++ high-level synthesis technology to our system-level solutions portfolio and broadens Synopsys' comprehensive solutions for block creation and optimization," said Joachim Kunkel, senior vice president and general manager of the Solutions Group at Synopsys. "It underscores Synopsys' clear commitment to being the leading EDA supplier of system-level solutions for SoC design, software development, hardware/software integration and system validation."

The terms of the deal, which closed today, have not been disclosed.

About Synopsys

Synopsys, Inc. (Nasdaq: SNPS) is a world leader in electronic design automation (EDA), supplying the global electronics market with the software, intellectual property (IP) and services used in semiconductor design, verification and manufacturing. Synopsys' comprehensive, integrated portfolio of implementation, verification, IP, manufacturing and field-programmable gate array (FPGA) solutions helps address the key challenges designers and manufacturers face today, such as power and yield management, system-to-silicon verification and time-to-results. These technology-leading solutions help give Synopsys customers a competitive edge in bringing the best products to market quickly while reducing costs and schedule risk. Synopsys is headquartered in Mountain View, California, and has more than 65 offices located throughout North America, Europe, Japan, Asia and India. Visit Synopsys online at <http://www.synopsys.com/>.

Safe Harbor Statement/Forward-Looking Statements

This press release contains forward-looking statements within the meaning of U.S. federal securities laws, including statements regarding the expected benefits of the transaction to Synopsys and its customers, and the plans to market new solutions. Forward-looking statements are subject to known and unknown risks and uncertainties that may cause actual results to differ materially from those expressed or implied in the forward-looking statements. These risks and uncertainties include, among others, Synopsys' ability to integrate the businesses and technologies and to manage the impact on financial results following the acquisition; customer demand for the new technologies and product offerings, as well as other risk factors as described in the Risk Factors section of Synopsys' most recently filed Quarterly Report on Form 10-Q. Synopsys assumes no obligation to update any forward-looking statement contained in this press release.

Synopsys, Galaxy and Synplify are registered trademarks of Synopsys, Inc. All other trademarks mentioned in this release are the intellectual property of their respective owners.

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