Synopsys Expands IP OEM Partner Program with Two New Members

Companies Select Broad Portfolio of High-Quality DesignWare IP as Preferred Solution for SoC Designs

MOUNTAIN VIEW, Calif., April 14 /PRNewswire-FirstCall/ -- Synopsys, Inc. (Nasdaq: SNPS), a world leader in software and IP for semiconductor design, verification and manufacturing, today announced that eSilicon and Brite Semiconductor have joined the Synopsys IP OEM Partner Program along with renewed members Global Unichip and Open-Silicon. Through the IP OEM Partner Program, members standardize on Synopsys' broad portfolio of silicon-proven DesignWare® interface and analog IP such as USB, PCI Express®, DDR, HDMI, SATA, Ethernet, MIPI IP including 3G DigRF, CSI-2, D-PHY, data converters and audio codecs for their system-on-chip (SoC) designs. By enabling member companies to access a wide range of interoperable IP from a single supplier, Synopsys helps them and their end-customers speed time-to-market and reduce risk for their complex SoC designs.

"With the growing trend towards outsourcing, Synopsys understands the need for an IP business model that enables eSilicon to deliver high-quality products to end-customers," said Patrick Soheili, vice president of marketing and business development at eSilicon. "As the pioneer of the Value Chain Producer business model, we have been using DesignWare IP for many years to identify the right match of IP for our broad customer base and this partner program is a formalization of that arrangement. Synopsys' proven DesignWare IP combined with eSilicon's proven first-pass silicon track record enables our customers to quickly ramp to volume production."

Synopsys enables fabless ASIC companies and supply chain partners to deliver right-first-time designs to their customers and help them meet their critical time-to-market windows. By providing the necessary resources and infrastructure, Synopsys enables the IP OEM Program members to deliver first-rate support to their end customers and easily create successful designs. Specialized on-site, in-depth product and integration training led by Synopsys engineers enables the partners to have an unprecedented level of expertise, resulting in first-pass silicon success.

"After evaluating IP vendors based on breadth of portfolio, quality of IP and technical support, we determined that Synopsys came out ahead in all areas," said Thomas Xu, vice president of engineering, at Brite Semiconductor. "As a member of Synopsys' IP OEM Partner Program, we are also able to develop a deeper level of expertise with their products, which will allow us to quickly integrate their IP into our customers' designs with less risk and improved time-to-market."

"Being a member of the IP OEM Partner Program has been instrumental in enabling us to quickly deliver differentiated designs to our end-customers," said Keh-Ching Huang, marketing director at Global Unichip. "Global Unichip has been focusing on providing service to extremely complex projects in advanced technology nodes. Our continued membership is a testament to the value provided by this program and demonstrates our continued commitment to speeding time-to-market and reducing risk for our customers."

"As an initial member of the Synopsys IP OEM Partner Program, Open-Silicon recognizes how a tighter working relationship between IP design and ASIC integration drives quality. Our close ties with Synopsys' knowledgeable technical support team have helped Open-Silicon revolutionize the ASIC industry and drive our OpenMODEL ASIC development process," said Scott Houghton, vice-president of marketing and business development at Open-Silicon. "This partnership has enabled us to deliver an excellent integration experience for our end customers."

"The expansion of the program membership demonstrates the success of the Synopsys IP OEM Partner Program and the value it provides to our members and their end-customers," said John Koeter, vice president of marketing for the Solutions Group at Synopsys. "By providing silicon-proven interface and analog IP solutions that are backed by an expert worldwide technical support team, Synopsys continues to help program members deliver high-quality designs that enable their customers to meet their aggressive time-to-market windows."

Availability

For more information on Synopsys' IP OEM members or to contact us for more information, visit: http://www.synopsys.com/dw/oempartners.php.

About DesignWare IP

Synopsys is a leading provider of high-quality, silicon-proven interface and analog IP solutions for system-on-chip designs. Synopsys' broad IP portfolio delivers complete connectivity IP solutions consisting of controllers,

PHY and verification IP for widely used protocols such as USB, PCI Express, DDR, SATA, Ethernet, HDMI and MIPI IP including 3G DigRF, CSI-2 and D-PHY. The analog IP family includes Analog-to-Digital Converters, Digital-to-Analog Converters, Audio Codecs, Video Analog Front Ends, Touch Screen Controllers and more. In addition, Synopsys offers SystemC transaction-level models to build virtual platforms for rapid, pre-silicon development of software. With a robust IP development methodology, extensive investment in quality and comprehensive technical support, Synopsys enables designers to accelerate time-to-market and reduce integration risk. For more information on DesignWare IP, visit: http://www.synopsys.com/designware. Follow us on Twitter at http://twitter.com/designware_ip.

About Synopsys

Synopsys, Inc. (Nasdaq: SNPS) is a world leader in electronic design automation (EDA), supplying the global electronics market with the software, intellectual property (IP) and services used in semiconductor design, verification and manufacturing. Synopsys' comprehensive, integrated portfolio of implementation, verification, IP, manufacturing and field-programmable gate array (FPGA) solutions helps address the key challenges designers and manufacturers face today, such as power and yield management, software-to-silicon verification and time-to-results. These technology-leading solutions help give Synopsys customers a competitive edge in bringing the best products to market quickly while reducing costs and schedule risk. Synopsys is headquartered in Mountain View, California, and has more than 65 offices located throughout North America, Europe, Japan, Asia and India. Visit Synopsys online at http://www.synopsys.com.

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