Synopsys Demonstrates DesignWare SuperSpeed USB 3.0 Controller IP at the SuperSpeed USB Developer's Conference

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Demonstration Shows Interoperability of Texas Instruments' Transceiver and Synopsys' DesignWare Digital Controller IP

MOUNTAIN VIEW, Calif., May 14 /PRNewswire-FirstCall/ --Synopsys, Inc. (NASDAQ: SNPS), a world leader in software and IP for semiconductor design and manufacturing, today announced its DesignWare® SuperSpeed USB 3.0 digital controller IP has tested successfully for interoperability with Texas Instrument's (TI) SuperSpeed USB 3.0 transceiver. The companies will demonstrate the joint solution at the upcoming SuperSpeed USB Developers Conference in Tokyo, Japan on May 20-21, 2009.

The demonstration will show real-world SuperSpeed USB data transfer of high-definition video from a PC to a device using the Synopsys DesignWare SuperSpeed USB 3.0 digital controller and TI's SuperSpeed USB 3.0 transceiver. Demonstrating proven multi-vendor interoperability accelerates the development of USB 3.0-enabled devices and allows consumers to benefit from the faster "sync-and-go" capability between PCs and the next generation of portable electronic devices. The DesignWare SuperSpeed USB 3.0 digital controller has also been successfully tested with the TI SuperSpeed USB 3.0 transceiver in the USB Implementers Forum (USB-IF) SuperSpeed Peripheral Interoperability Lab.

"Synopsys provides a robust, high-quality SuperSpeed USB 3.0 IP solution," said Dan Jensen, Connectivity Interface Solutions Manager at TI. "In addition to Synopsys' long history of delivering USB-compliant IP, these elements are important to helping us lower integration risk and ensure our transceiver interoperates with new USB 3.0 products and existing USB 2.0 devices."

"Synopsys is at the forefront of providing IP that enables leading semiconductor companies to incorporate the latest functionality into their advanced integrated circuits," said John Koeter, Vice president of Marketing for the Solutions Group at Synopsys. "By working closely with TI we are enabling designers to take advantage of SuperSpeed USB 3.0 data rates and quickly create innovative products with less risk and improved time-to market."

Availability

The DesignWare SuperSpeed USB 3.0 digital controller is shipping to early adopters now.

To see actual USB 3.0 data transfer in the Synopsys lab and for more information on the complete DesignWare SuperSpeed USB IP solution, including digital controllers, PHY, verification IP, virtual platform and drivers, visit http://www.synopsys.com/usb/.

Also, visit the USB IP blog at http://www.synopsysoc.org/usb-blog/.

About DesignWare IP

Synopsys is the leading provider of high-quality, silicon-proven interface and analog IP solutions for system-on-

chip designs. Synopsys' broad IP portfolio delivers complete connectivity IP solutions consisting of controllers, PHY and verification IP for widely used protocols such as USB, PCI Express, DDR, SATA, HDMI and Ethernet. The analog IP family includes Analog-to-Digital Converters, Digital-to-Analog Converters, Audio Codecs, Video Analog Front Ends, Power Management solutions and more. In addition, Synopsys offers SystemC transaction-level models to build virtual platforms for rapid, pre-silicon development of software. With a robust IP development methodology, extensive investment in quality and comprehensive technical support, Synopsys enables designers to accelerate time-to-market and reduce integration risk. For more information on DesignWare IP, visit http://www.synopsys.com/designware/.

About Synopsys

Synopsys, Inc. (NASDAQ: SNPS) is a world leader in electronic design automation (EDA), supplying the global electronics market with the software, intellectual property (IP) and services used in semiconductor design and manufacturing. Synopsys' comprehensive, integrated portfolio of implementation, verification, IP, manufacturing and field-programmable gate array (FPGA) solutions helps address the key challenges designers and manufacturers face today, such as power and yield management, system-to-silicon verification and time-to-results. These technology-leading solutions help give Synopsys customers a competitive edge in bringing the best products to market quickly while reducing costs and schedule risk. Synopsys is headquartered in Mountain View, California, and has more than 60 offices located throughout North America, Europe, Japan, Asia and India. Visit Synopsys online at http://www.synopsys.com/.

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