

Creative Chips Adopts Synopsys' Galaxy Custom Designer Mixed-Signal Implementation Solution

Modern Architecture and Unified Design and Verification Environment Key to Productivity

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MOUNTAIN VIEW, Calif., Nov. 24 /PRNewswire-FirstCall/ -- Synopsys, Inc. (NASDAQ: SNPS), a world leader in software and IP for semiconductor design and manufacturing, today announced that Creative Chips has adopted Synopsys' Galaxy Custom Designer™ custom implementation solution. As a designer of systems for the automotive and consumer markets, Creative Chips needs a modern-era solution that unifies cell-based and custom design. Custom Designer provides a comprehensive digital and analog design flow that helped enable Creative Chips to implement complex product designs and meet critical time-to-market, cost, and quality constraints.

"The devices we design for our customers include diverse analog components coupled with complex digital systems and signal processing," said Dr. Lutz Porombka, managing director for Creative Chips. "Using Custom Designer, we can design, simulate and verify large cell-based blocks and high-performance analog circuits on a single platform. Synopsys' unified custom design environment significantly enhances our productivity and design quality, enabling us to meet our customers' expectations for silicon that is on budget and works the first time."

Built from the ground up, Custom Designer was architected for productivity. Key modules include a schematic editor featuring on-canvas editing and dynamic net highlighting. The simulation environment provides a common use model allowing access to Synopsys simulators, including the HSPICE®, HSIM™ XA, NanoSim® XA and WaveView Analyzer simulators. The layout editor features a real-time preview of P-Cell parameter changes, and the results from Synopsys' Hercules™ DRC/LVS and Star-RCXT™ parasitic extraction tools are dynamically available within Custom Designer. In addition, Custom Designer exchanges vital information with Synopsys' IC Compiler physical implementation solution during floorplanning, placement, routing and final chip editing to reduce time-consuming design iterations.

"Creative Chips' successful adoption of Custom Designer underscores Synopsys' focus on serving markets, like automotive and consumer products, where there is increasing analog content integrated into complex digital designs," said Bijan Kiani, vice president of Marketing at Synopsys. "We are working closely with companies like Creative Chips to enable their success in the highly competitive and demanding markets they serve."

About Synopsys

Synopsys, Inc. (NASDAQ: SNPS) is a world leader in electronic design automation (EDA), supplying the global electronics market with the software, intellectual property (IP) and services used in semiconductor design and manufacturing. Synopsys' comprehensive, integrated portfolio of implementation, verification, IP, manufacturing and field-programmable gate array (FPGA) solutions helps address the key challenges designers and manufacturers face today, such as power and yield management, system-to-silicon verification and time-to-results. These technology-leading solutions help give Synopsys customers a competitive edge in bringing the best products to market quickly while reducing costs and schedule risk. Synopsys is headquartered in Mountain View, California, and has more than 60 offices located throughout North America, Europe, Japan, Asia and India. Visit Synopsys online at <http://www.synopsys.com/>.

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