

Euphonic Technologies Achieves First-Pass Silicon Success With DesignWare IP for PCI Express

Comprehensive, High-Quality PCI Express IP Solution Meets Low Power and High Performance Goals for Embedded Graphics Controller

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MOUNTAIN VIEW, Calif., July 8 /PRNewswire-FirstCall/ -- Synopsys, Inc. (NASDAQ: SNPS), a world leader in software and IP for semiconductor design and manufacturing, today announced that Euphonic Technologies has achieved first-pass silicon success for its high-performance EGC601 Embedded Graphics Controller system-on-chip (SoC) utilizing Synopsys' DesignWare® IP for PCI Express. Euphonic selected Synopsys' silicon-proven DesignWare IP because it offered the most comprehensive suite of digital controllers and mixed-signal PHY that met their power, feature and performance requirements. Furthermore, Euphonic benefitted from obtaining all the PCI Express IP from a single vendor and had convenient access to an experienced local technical support team, which enabled them to lower integration risk and speed time to market.

Euphonic needed an eight-lane PCI Express interface to meet the high bandwidth requirements of the EGC601 graphics controller, which supports up to five different imaging layers without intervention of the host CPU. After evaluating several IP providers, Euphonic chose Synopsys because the DesignWare IP for PCI Express exceeded the performance requirements and was extremely low in power consumption. In addition, Euphonic was impressed with the on-chip diagnostic and ATE capabilities of the PHY, which allowed them to get from initial silicon to production test within a matter of weeks. According to Euphonic, Synopsys' offering was unparalleled.

"Selecting Synopsys was definitely the right decision," said Shinji Masuda, vice president of engineering at Euphonic Technologies. "The DesignWare IP for PCI Express met our latency, power and performance requirements. Furthermore, the quality of the IP was excellent, enabling us to completely focus our engineering resources on product differentiation and achieve first-pass silicon success."

"We understand the importance of providing customers such as Euphonic Technologies with complete and compliant IP solutions that help them succeed in an aggressive market," said John Koeter, senior director of marketing for IP and Services at Synopsys. "As a leading provider of connectivity IP, we invest heavily in developing high-quality PCI Express IP that meets our customers' design requirements and reduces integration risk for their complex SoCs."

About DesignWare IP

Synopsys offers a broad portfolio of high-quality, silicon-proven digital, mixed-signal and verification IP for system-on-chip designs. As a leading provider of connectivity IP, Synopsys delivers the industry's most complete solutions for widely used protocols such as USB, PCI Express, SATA, Ethernet and DDR. The DesignWare IP is optimized for low power and is an integral part of the Synopsys Eclipse low power solution. In addition to connectivity IP, Synopsys offers SystemC transaction level models to build virtual platforms for rapid, pre-silicon development of software. When combined with a robust IP development methodology, extensive investment in quality and comprehensive technical support, DesignWare IP enables designers to accelerate time-to-market and reduce integration risk. For more information on DesignWare IP, visit <http://www.synopsys.com/designware/>.

About Synopsys

Synopsys, Inc. (NASDAQ: SNPS) is a world leader in electronic design automation (EDA), supplying the global electronics market with the software, intellectual property (IP) and services used in semiconductor design and manufacturing. Synopsys' comprehensive, integrated portfolio of implementation, verification, IP, manufacturing and field-programmable gate array (FPGA) solutions helps address the key challenges designers and manufacturers face today, such as power and yield management, system-to-silicon verification and time-to-results. These technology-leading solutions help give Synopsys customers a competitive edge in bringing the best products to market quickly while reducing costs and schedule risk. Synopsys is headquartered in Mountain View, California, and has more than 60 offices located throughout North America, Europe, Japan, Asia and India. Visit Synopsys online at <http://www.synopsys.com/>.

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