Cedar Point Utilizes Synopsys' VCS Native Testbench Technology to Speed Time to Market for Safari C(3) VoIP Switch

Advanced Testbench Techniques Critical for Multi-Protocol Verification Environment at Chip and System Levels

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Synopsys, Inc. (NASDAQ: SNPS), a world leader in semiconductor design software, today announced that Cedar Point Communications, the leader in integrated packet-based voice switching technologies for the cable industry, has used the Native Testbench (NTB) technology in Synopsys' VCS® comprehensive RTL verification solution to simplify testing and speed time to market for its SAFARI C(3) VoIP switch. SAFARI C(3) Multimedia Switching System is the only totally integrated carrier class switch that incorporates -- at no additional cost -- all of the components that make up the PacketCable™ voice switching infrastructure. The VCS solution is a key component of the Synopsys Discovery™ Verification Platform.

"Our advanced field programmable gate array (FPGA) designs require sophisticated verification tools and techniques to help ensure optimum performance for our customers," said Kostas Kalaitzidis, lead FPGA verification engineer at Cedar Point. "Synopsys' VCS NTB technology allowed us to easily write testbench drivers and monitors for a wide variety of complex packet-based protocols and to develop directed and randomized tests that reflect cable operators' field needs."

VCS NTB technology with support for the SystemVerilog and OpenVera® languages deploys a unique, single-compiler architecture to simultaneously optimize design, testbench, assertions and coverage, delivering up to five times faster verification performance compared to independent testbench and simulation environments. VCS NTB supports advanced testbench capabilities such as constrained-random stimulus generation, object-oriented programming, advanced data types and more. The VCS solution's built-in code and functional coverage engines help ensure more predictable verification closure by enabling engineers to accurately monitor verification progress over time, and to determine when verification is complete.

"Synopsys' VCS NTB technology is unique in the industry," said Farhad Hayat, vice president of Marketing, Verification Group, Synopsys, Inc. "It brings advanced verification methods, including coverage, assertions and testbench automation, directly into the simulator to yield a fast, comprehensive verification solution. Our customers are routinely seeing speedups of up to five times when they use VCS NTB."

Synopsys Discovery Verification Platform

The Discovery Verification Platform is a unified environment that provides high performance and efficiency of interaction among all platform components, including mixed-HDL simulation, mixed-signal, system-level verification, assertions, DesignWare® verification intellectual property, code coverage, functional coverage, testbenches and formal analysis. Combined with support for industry-standard hardware design and verification languages, including Verilog, VHDL, SystemVerilog, SystemC™ and OpenVera, and Synopsys' proven Reference Verification Methodology, the Discovery Verification Platform helps designers achieve higher levels of verification productivity by contributing to first-time silicon success within required project cycles.

About Synopsys

Synopsys, Inc. is a world leader in electronic design automation (EDA) software for semiconductor design. The company delivers technology-leading semiconductor design and verification platforms and IC manufacturing software products to the global electronics market, enabling the development and production of complex systems-on-chips (SoCs). Synopsys also provides intellectual property and design services to simplify the design process and accelerate time-to-market for its customers. Synopsys is headquartered in Mountain View, California and has offices in more than 60 locations throughout North America, Europe, Japan and Asia. Visit Synopsys online at http://www.synopsys.com/.

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Editorial Contacts:
Isela Warner
Synopsys, Inc.
650-584-1644

igamboa@synopsys.com

Suraya Akbarzad
Edelman
650-968-4033
suraya.akbarzad@edelman.com

SOURCE: Synopsys, Inc.

CONTACT: Isela Warner of Synopsys, Inc., +1-650-584-1644, or igamboa@synopsys.com; or Suraya Akbarzad of Edelman, +1-650-968-4033, or suraya.akbarzad@edelman.com, for Synopsys

Web site: http://www.synopsys.com/