Synopsys and Si2 Drive Open-Source Library Modeling to Next Level by Forming Technical Advisory Board

Liberty Technical Advisory Board to Build on Composite Current Source Modeling Technology to Address Future Industry Needs

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Synopsys, Inc. (NASDAQ: SNPS), a world leader in semiconductor design software, and The Silicon Integration Initiative (Si2), a semiconductor industry consortium, today announced the formation of a technical advisory board (TAB) to facilitate the evolution of the open-source Liberty™ library modeling standard. For the past 15 years, Synopsys has led the industry in library modeling technology as well as in pioneering programs and initiatives for open source standards. Given the significant industry-wide momentum with the new Liberty Composite Current Source (CCS) modeling technology, the Liberty TAB has been formed to enable representatives from the design community, electronic design automation (EDA) companies, silicon foundries, and library modeling suppliers to guide the evolution of the Liberty standard. To receive more information and see live presentations on this initiative, please register to attend the 17th EDA Interoperability Developers' Forum on May 17th, 2006. Visit: http://www.synopsys.com/devforum/may2006 .

The Liberty library format is the de-facto modeling standard in the industry with support from more than 60 EDA vendors with over 750 libraries in production. It has a long-standing history of innovation to address the latest silicon technology modeling requirements. Today, the Liberty standard enables accurate and comprehensive modeling of nanometer effects, including timing, noise, power, and test. In 2000, the Liberty standard became among the first EDA standards to be provided as Open Source, to foster innovation and interoperability for a wide-variety of EDA applications.

"Si2's members share a vision to leverage broad industry interaction on advanced modeling requirements to create innovative solutions offering greater efficiency in the marketplace," said Steve Schulz, president and CEO of the Silicon Integration Initiative. "The creation of this TAB further opens the Liberty modeling standard to this broad industry input. We firmly believe that this will greatly benefit the EDA and semiconductor industries, and we are committed to working with Synopsys to make this initiative a great success along with our members."

"For many years, Synopsys has worked closely with designers as well as with EDA and IP vendors to help ensure that the Liberty standard meets the advanced modeling requirements of the latest silicon technologies," said Antun Domic, senior vice president and general manager of Synopsys' Implementation Group. "Having achieved widespread adoption of the CCS modeling technology, we believe that the best way to evolve this technology to meet future demands is through the Liberty TAB."

The TAB will consist of approximately 12 members, each elected for three- year terms. Member companies will represent EDA, end users, foundries and library (IP) providers. Requests for enhancements can come from the membership as well as from the overall Liberty user base. To apply for membership in the TAB, visit www.si2.org/Liberty-TAB.

About Synopsys

Synopsys, Inc. is a world leader in EDA software for semiconductor design. The company delivers technology-leading semiconductor design and verification platforms and IC manufacturing software products to the global electronics market, enabling the development and production of complex systems-on-chips (SoCs). Synopsys also provides intellectual property and design services to simplify the design process and accelerate time-to-market for its customers. Synopsys is headquartered in Mountain View, California and has offices in more than 60 locations throughout North America, Europe, Japan and Asia. Visit Synopsys online at http://www.synopsys.com/ .

About Si2

Si2 is an organization of industry-leading semiconductor, systems, EDA and manufacturing companies focused on improving the way integrated circuits are designed and manufactured in order to speed time-to market, reduce costs, and meet the challenges of sub-micron design. Si2 is uniquely positioned to enable collaboration through a strong implementation focus driven by its member companies. Si2 focuses on developing practical technology solutions to industry challenges. Si2 represents over 100 companies involved in all parts of the silicon supply chain throughout the world. For more information on Si2, visit: www.si2.org.

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Editorial Contacts:
Gita Rao-Prasad
Synopsys, Inc.
650-584-1441
gitar@synopsys.com

Tara Yingst
Edelman
650-429-2731
Tara.yingst@edelman.com

Bill Bayer
Director, Marketing Communications
Silicon Integration Initiative (Si2)
512-342-2244x304
bill@si2.org

SOURCE: Synopsys, Inc.

CONTACT: Gita Rao-Prasad of Synopsys, Inc., +1-650-584-1441, or gitar@synopsys.com; or Tara Yingst of Edelman, +1-650-429-2731, or Tara.yingst@edelman.com, for Synopsys, Inc.; or Bill Bayer, Director, Marketing Communications of Silicon Integration Initiative (Si2), +1-512-342-2244, ext. 304, or bill@si2.org

Web site: http://www.synopsys.com/