

Silicon Optix Selects Synopsys IC Compiler for 90nm Design

Ease of Migration, Strong Technology Roadmap Drive Competitive Selection

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Synopsys, Inc. (NASDAQ: SNPS), a world leader in semiconductor design software, today announced that Silicon Optix has adopted the Synopsys IC Compiler next-generation place-and-route solution for its high-performance video processor designs. Silicon Optix has long been a user of Synopsys place-and-route technology. With the move to 90 nanometers (nm), Silicon Optix reviewed all available solutions and selected IC Compiler for its ability to deliver the necessary performance while posing low migration risk and providing a superior roadmap for future technology innovation.

"Silicon Optix has been growing at a very rapid pace and we are actively investing in the latest design technology to continue our momentum," said Kent Goodin, executive vice president of Engineering and Technology at Silicon Optix. "Over the next year, we are planning to produce many variants of our leading-edge video processors. We expect IC Compiler to play a key role by enabling a path to enhanced performance while improving the productivity of our designers and simultaneously minimizing transition risks."

Silicon Optix was recently rated by Deloitte as one of the top 20 fastest-growing technology companies in Silicon Valley. Silicon Optix is currently deploying IC Compiler on the next generation of their flagship video processors for high-end applications, as well as mainstream consumer applications. Besides advanced timing and power optimizations and overall 90nm readiness, one of the key benefits for Silicon Optix is IC Compiler's very high correlation with Synopsys' PrimeTime® golden sign-off solution. In addition, IC Compiler introduces the capability to optionally bring in exact data from PrimeTime to drive final optimizations, thus speeding timing closure for complex designs.

"IC Compiler is an ideal choice for demanding applications such as video processor designs. The performance and productivity benefits of IC Compiler should enable Silicon Optix to deliver a variety of leading-edge products to the market faster," said Bijan Kiani, vice president of Marketing for Synopsys' Implementation Group. "We welcome Silicon Optix to the growing ranks of IC Compiler users and look forward to working together to enable silicon success."

About IC Compiler

The IC Compiler tool is Synopsys' next-generation place-and-route solution. It provides superior results and faster time-to-results by extending physical synthesis to full place-and-route, and by enabling signoff-driven design closure. Current-generation solutions have a limited horizon because placement, clock tree, and routing are separate, disjointed operations. IC Compiler's Extended Physical Synthesis (XPS) technology breaks down the walls between these steps by extending physical synthesis to full place-and-route. IC Compiler has a unified, TCL-based architecture that implements innovations and harnesses some of the best Synopsys core technologies. It is a complete place- and-route system with everything necessary to do next-generation designs, including physical synthesis, placement, routing, timing, signal integrity (SI) optimization, power reduction, design-for-test (DFT), and yield optimization.

Silicon Optix is a leader in programmable high-performance video processors.

About Synopsys

Synopsys, Inc. is a world leader in electronic design automation (EDA) software for semiconductor design. The company delivers technology-leading semiconductor design and verification platforms and IC manufacturing software products to the global electronics market, enabling the development and production of complex systems-on-chips (SoC's). Synopsys also provides intellectual property and design services to simplify the design process and accelerate time-to-market for its customers. Synopsys is headquartered in Mountain View, California, and has more than 60 offices located throughout North America, Europe, Japan and Asia. Visit Synopsys online at www.synopsys.com.

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