Sasken Develops Reference Flow Based on Synopsys' Galaxy Design Platform

Sasken Flow Delivers a Time to Market Advantage for Customers by Accelerating Design Convergence

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Synopsys, Inc. (NASDAQ: SNPS), the world leader in semiconductor design software, today announced that Sasken, a world-class embedded telecommunications technology solution provider, has used Synopsys' Galaxy™ Design Platform to develop a reference flow to enhance the implementation and signoff process for its complex designs -- accelerating design convergence and delivering a time to market advantage for customers.

"With Synopsys' Galaxy Design Platform, Sasken has further enhanced its ability to handle complex designs from various foundries using different process technologies to tape out," said Vilas Bhade, director, Sasken Communication Technology. "The Synopsys Galaxy solution enables Sasken to deliver to its customers a time-to-market advantage."

Sasken's custom reference flow now includes the following Galaxy technology from Synopsys: Design Compiler® synthesis tool, JupiterXT™ design planning tool, Physical Compiler® physical synthesis tool and Astro™ place and route product for physical implementation, PrimeTime® timing analysis tool, DFT Compiler™ design for test tool, TetraMAX® test product, Star-RCXT™ tool for full-chip parasitic extraction sign-off, and Hercules™ physical verification product.

In addition, Sasken has added Synopsys' NanoSim® simulation tool to its mixed-signal design and verification services, and has adopted Synopsys' HSPICE® circuit simulator tool, CosmosSE™ simulation and analysis solution, and CosmosLE™ intelligent layout automation software for custom IC design products.

"Synopsys is committed to delivering best-in-class technology to customers such as Sasken for their toughest ASIC designs," said Dr. Howard Ko, vice president of Asia Pacific at Synopsys. "The Synopsys Galaxy solution provides an integrated platform with comprehensive support at all stages of the design flow -- from implementation to sign-off."

About Sasken

Sasken is a world class embedded telecom technology solution provider offering a unique combination of complementary IP software components, research and development consultancy as well as software services to many of the leading semiconductor manufacturers, network equipment companies, and global wireless handset developers. Established in 1989, Sasken employs over 1900 staff, operating from a state of the art research and development centre in Bangalore & Pune, India with offices in Canada, China, Germany, France, Japan, Sweden, U.K. and U.S. http://www.sasken.com/ .

About Synopsys

Synopsys, Inc. is the world leader in electronic design automation (EDA) software for semiconductor design. The company delivers technology-leading semiconductor design and verification platforms and IC manufacturing software products to the global electronics market, enabling the development and production of complex systems-on-chips (SoCs). Synopsys also provides intellectual property and design services to simplify the design process and accelerate time-to-market for its customers. Synopsys is headquartered in Mountain View, California, and has more than 60 offices located throughout North America, Europe, Japan and Asia. Visit Synopsys online at http://www.synopsys.com/ .

NOTE: Synopsys, Design Compiler, HSPICE, NanoSim, Physical Compiler, PrimeTime, and TetraMAX are registered trademarks of Synopsys, Inc., and Astro, CosmosSE, CosmosLE, DFT Compiler, Galaxy, Hercules, JupiterXT, and Star-RCXT are trademarks of Synopsys, Inc. All other trademarks or registered trademarks mentioned in this release are the intellectual property of their respective owners.

SOURCE: Synopsys, Inc.

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