

Synopsys Agrees to Acquire Analog Design Automation

ADA's Circuit Optimization Solution Will Help Deliver Significant Productivity Gains Through Tight Integration With HSPICE

PRNewswire-FirstCall
MOUNTAIN VIEW, Calif.

Synopsys, Inc. (NASDAQ: SNPS), the world leader in semiconductor design software, today announced that it has signed a definitive agreement to acquire substantially all of the assets of Analog Design Automation, Inc. (ADA), a leading provider of automated circuit optimization solutions for analog, mixed-signal and custom integrated circuits, thereby extending Synopsys' analog and mixed-signal offering. Through this acquisition, Synopsys expects to introduce new and innovative analog and mixed-signal design technologies. ADA's current offerings automate analog and mixed-signal design creation which helps reduce design cycle time. The terms of the deal are not being disclosed.

"Synopsys evaluated a number of circuit optimization solutions available in the market and believes that ADA's technology offered the best combination of features and performance when compared to the alternatives," said Sanjiv Kaul, senior vice president and general manager, New Ventures, Synopsys, Inc. "Selling ADA circuit optimization technology standalone, and more importantly integrating it with Synopsys' HSPICE® and NanoSim™ circuit simulators, will strengthen our solutions in the analog and mixed-signal area by offering customers a powerful, multifunctional simulation tool."

"Synopsys was an early investor in ADA and there is strong synergy between ADA's analog optimization and Synopsys' circuit simulation products," said Matthew Raggett, CEO of Analog Design Automation. "This acquisition will help take the most advanced optimization technology to the mainstream analog design market."

"ADA products have provided a clear benefit for our analog designers," stated Mike Carter, manager, IC CAD and Methodologies at Potentia Semiconductor. "ADA provides a powerful combination of a simulation environment, a circuit optimization engine and circuit characterization capabilities. Using ADA tools and HSPICE, we have been able to significantly accelerate our analog design process and simultaneously improve design quality."

In the acquisition, Synopsys will acquire all of ADA's analog and mixed-signal tools and technology, and will hire its technical, applications and sales team. The acquisition is subject to customary closing conditions, including receipt of requisite ADA shareholder approvals, and is expected to close by the end of February 2004.

Forward Looking Statements

This press release contains forward-looking statements within the meaning of the safe harbor provisions of Section 21E of the Securities Exchange Act of 1934, including statements regarding the expected benefits of the combination of ADA's analog and mixed-signal design tools and Synopsys' circuit simulation products and the expected closing date of the acquisition. These statements are based on Synopsys' current expectations and beliefs. Actual results could differ materially from the results implied by these statements as a result of unforeseen difficulties in integrating ADA's tools and delays in obtaining ADA shareholder approval for the acquisition.

About Synopsys

Synopsys, Inc. is the world leader in electronic design automation (EDA) software for semiconductor design. The Company delivers technology-leading semiconductor design and verification platforms to the global electronics market, enabling the development of complex systems-on-chips (SoCs). Synopsys also provides intellectual property and design services to simplify the design process and accelerate time-to-market for its customers. Synopsys is headquartered in Mountain View, California and has offices in more than 60 locations throughout North America, Europe, Japan and Asia. Visit Synopsys online at <http://www.synopsys.com/>.

NOTE: NanoSim is a trademark and Synopsys and HSPICE are registered trademarks of Synopsys, Inc. All other products mentioned in this release are the intellectual property of their respective owners.

SOURCE: Synopsys, Inc.

CONTACT: media, Yvette Huygen of Synopsys, Inc., +1-650-584-4547, or yvetteh@synopsys.com; or Andrea Zils of Edelman, +1-650-429-2731, or andrea.zils@edelman.com, for Synopsys, Inc.; or investors, Jessica Kourakos of Synopsys, Inc., +1-650-584-4289, or jessicak@synopsys.com

Web site: <http://www.synopsys.com/>
