Synopsys PrimeTime and Star-RCXT Solutions Deployed at Fujitsu as Standard for 65-Nanometer Sign-Off

Extends Usage of Gold Standard Sign-off Solutions to 65-nm ASIC and COT Design Flows

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Synopsys, Inc. (NASDAQ: SNPS), a world leader in semiconductor design software, today announced that Fujitsu Limited has standardized on Synopsys' PrimeTime® and Star-RCXT™ products as the timing sign-off solution for its 65-nanometer (nm) ASIC and COT design flows. The seamless integration of static timing analysis, delay calculation and interconnect parasitic extraction combined with the high accuracy delivered by Synopsys' gold-standard tools will enable Fujitsu and its customers to achieve faster design closure.

"A sign-off solution that ensures silicon accuracy while providing high performance and capacity is extremely important to our design flows at 65 nm and below," said Shoji Ichino, general manager, Design Platform Development Division, Electronic Devices Business Unit of Fujitsu Limited. "We extended our usage of the Star-RCXT and PrimeTime products to our 65-nm process because of Star-RCXT's proven sub-femtofarad-accurate extraction technology and PrimeTime's advanced analysis capabilities for designs at the 65-nm process nodes. Together, the Star-RCXT and PrimeTime products provide the rich set of features, ease-of-use and speed that are already delivering significant productivity benefits on our largest, most advanced production designs."

"Synopsys continues to advance its proven timing analysis and extraction capabilities and deliver the best-in-class sign-off solution for our customers at the 65-nm process node," said Antun Domic, senior vice president and general manager of Synopsys' Implementation Group. "Fujitsu's adoption of the combined PrimeTime and Star-RCXT solution for their 65-nm design flows once again demonstrates the trust that leading semiconductor companies have placed in our gold standard sign-off solution. We look forward to continue working closely with Fujitsu to extend the benefits of our solution to process nodes beyond 65 nm."

About Synopsys

Synopsys, Inc. (NASDAQ: SNPS) is a world leader in electronic design automation (EDA) software for semiconductor design. The company delivers technology-leading semiconductor design and verification platforms and IC manufacturing software products to the global electronics market, enabling the development and production of complex systems-on-chips (SoCs). Synopsys also provides intellectual property and design services to simplify the design process and accelerate time-to-market for its customers. Synopsys is headquartered in Mountain View, California, and has more than 60 offices located throughout North America, Europe, Japan and Asia. Visit Synopsys online at www.synopsys.com.

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