Advisory: Synopsys, ARM, Artisan and TSMC Host Luncheon Panel at DAC on Design-to-Silicon Power Management

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SAN FRANCISCO

Synopsys, Inc. (NASDAQ: SNPS), the world leader in semiconductor design software, ARM (NASDAQ: ARMHY), the industry's leading provider of 16/32-bit embedded RISC processor solutions, Artisan Components, Inc., (NASDAQ: ARTI) a leading provider of physical intellectual property, and Taiwan Semiconductor Manufacturing Company Ltd. (TSMC) (NYSE: TSM), the world's largest dedicated semiconductor foundry, will jointly host a DAC 2004 luncheon panel on power management on Tuesday, June 8, 2004 from noon to 2 p.m. PT.

What:

EE Times editor Ron Wilson will moderate an open-microphone discussion amongst luncheon attendees and these distinguished panel members:

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-- Mike Muller, Chief Technology Officer, ARM
-- Dhrumil Gandhi, Senior Vice President / Product Technology, Artisan
-- Antun Domic, PhD, Senior Vice President & General Manager /
   Implementation Group, Synopsys
-- John Yue, PhD, Vice President / Technology, TSMC North America
-- Peter Henry, Vice President / Power Products Group, National
   Semiconductor
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The panel discussion will focus on addressing the need for power-efficient design in today's feature-rich portable systems, and will look at how designers today can optimize system-wide power use to reduce power consumption and maximize battery life.

Panelists will share how they are working together to drive innovations in power management, offering insights into their newest performance-enhancing techniques.

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When:
Tuesday, June 8, 2004 from noon to 2 p.m. PT
For more information and to register, visit
 http://www.synopsys.com/news/events/dac2004/powerlunch.html
Registration closes June 4, 2004.

Where:
DAC 2004, San Diego Convention Center, Meeting Room 25A-C
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Power management presentations will also be given at the following times in Synopsys' DAC Partner Booth located at #4731:

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-- ARM and Synopsys Team for Power-Efficient SoC Design
   Monday, June 7 and Tuesday, June 8 from 4 to 5 p.m.
-- Artisan Physical IP and Synopsys Galaxy Design Platform Foster
   Innovation
   Monday, June 7 and Tuesday, June 8 from 3 to 4 p.m.
-- TSMC Reference Flow 5.0: Integrating Design and Process
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Monday, June 7, Tuesday, June 8 and Wednesday, June 9 from 2 to 3 p.m.

Who Should Attend:

Designers who are looking for ways to optimize system-wide power use and maximize battery life; those interested in how to control supply voltage in multiple domains and how mixing Vt enables tradeoffs between power and speed.

About Synopsys

Synopsys, Inc. is the world leader in electronic design automation (EDA) software for semiconductor design. The company delivers technology-leading semiconductor design and verification platforms and IC manufacturing software products to the global electronics market, enabling the development and production of complex systems-on-chips (SoCs). Synopsys also provides intellectual property and design services to simplify the design process and accelerate time-to-market for its customers. Synopsys is headquartered in Mountain View, California, and has more than 60 offices located throughout North America, Europe, Japan and Asia. Visit Synopsys online at http://www.synopsys.com/ .

About ARM

ARM is the industry's leading provider of 16/32-bit embedded RISC microprocessor solutions. The company licenses its high-performance, low-cost, power-efficient RISC processors, peripherals and system-chip designs to leading international electronics companies. ARM also provides comprehensive support required in developing a complete system. ARM's microprocessor cores are rapidly becoming the volume RISC standard in such markets as portable communications, hand-held computing, multimedia digital consumer and embedded solutions. More information on ARM is available at http://www.arm.com/ .

About Artisan Components

Artisan Components, Inc. is a leading provider of physical intellectual property (IP) components for the design and manufacture of complex system-on-a-chip integrated circuits. Artisan's products include embedded memory, standard cell, input/output, analog and mixed-signal components, which are designed to achieve the best combination of performance, density, power and yield for a given manufacturing process. Artisan has licensed its IP components to over 1,200 companies involved in integrated circuit design. Artisan is headquartered in Sunnyvale, California. More information about Artisan Components, including free library access, can be found at http://www.artisan.com/ .

About TSMC

TSMC is the world's largest dedicated semiconductor foundry, providing the industry's leading process technology and the foundry industry's largest portfolio of process-proven library, IP, design tools and reference flows. The company operates one advanced 300-mm wafer fab, five eight-inch fabs and one six-inch wafer fab. TSMC also has substantial capacity commitments at its wholly-owned subsidiary, WaferTech, and its joint venture fab, SSMC. In early 2001, TSMC became the first IC manufacturer to announce a 90-nm technology alignment program with its customers. TSMC's corporate headquarters are in Hsinchu, Taiwan. For more information about TSMC please see http://www.tsmc.com/ .

SOURCE: Synopsys, Inc.

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