Advisory/ Synopsys to Demonstrate First Controller IP Based on Certified Wireless USB Specification at 2006 Certified Wireless USB Developers Conference

PRNewswire

Synopsys, Inc. (NASDAQ: SNPS), a world leader in semiconductor design software, will demonstrate the industry's first commercially available device controller based on the Certified Wireless USB specification from the USB Implementers Forum (USB-IF) at the Certified Wireless USB Developers Conference (DevCon) in San Jose, California on June 20-22, 2006. Synopsys' DesignWare® WiUSB Device intellectual property (IP) is designed for use in portable electronics by minimizing power consumption. The Device IP also reduces overall design cost, and minimizes risk for designers adding Certified Wireless USB technology. The Certified Wireless USB DevCon is the premier technical event for engineers looking to learn from the creators of Certified Wireless USB and to learn how to incorporate Certified Wireless USB into product roadmaps.

WHAT: Visit Synopsys at the Certified Wireless USB DevCon and see how the world leader in standards-based connectivity IP can help with your design challenges. Synopsys' Device IP is now available for design into system-on chips (SoCs) and will interface to standard WiMedia Ultra Wideband (UWB) PHYs. Synopsys will be showcasing its latest Device IP based on the Certified Wireless USB Specification from the USB-IF. In addition, Matt Myers, who specializes in Certified Wireless USB Architecture at Synopsys, will be presenting new in-depth material on how to build Certified Wireless USB dual- role devices and devices that can connect to multiple PCs simultaneously. Synopsys will update attendees on the latest technical details of the Synopsys IP based on Certified Wireless USB.

WHEN: June 20-22, 2006

WHERE: San Jose Convention Center, 150 W. San Carlos St., San Jose, CA 95113.

CERTIFIED WIRELESS USB DEVCON EXHIBIT HOURS:
  Tuesday, June 20, 6:00 pm - 9:00 pm
  Wednesday, June 21, 6:00 pm - 9:00 pm
  Thursday, June 22, 12:30 pm - 1:30 pm

SYNOPSYS HIGHLIGHTS AT CERTIFIED WIRELESS USB DEVCON:

-- Synopsys Complete Wireless Device IP Solution
  Synopsys will update attendees on the technical details of the
  Synopsys-built and verified controller. The core is firmware-
  programmable and designed for power management. Firmware
  programmability enables designers to add and remove features or fix
  bugs after silicon implementation. The Synopsys Device IP demonstration
  consists of the Synopsys controller implemented in a multimedia player
  reference design. Pictures and video from the reference design will be
  transferred into a PC with the assistance of the next generation
  Peripheral Developers Kit (PDK) for Certified Wireless. For more
  information on the complete DesignWare IP solution based on Certified
  Wireless USB, please visit:
  https://www.synopsys.com/designware-ip/interface-ip/usb.html .

-- Design Considerations for Self-Beaconing and Dual-Role Devices:
  Presentation by Matt Myers, Synopsys, Inc.
  Self-Beaconing adds the ability for a product, like a printer, to
  associate with multiple PCs or cameras. This added functionality is
  driven by consumers who want to simply share a peripheral wirelessly.
  Dual-role devices can be devices like mobile phones that synchronize
  with a PC, but this device may want to print directly to a printer. The
  phone is a device to the PC, and at the same time, a host to the
  printer. Consumers are demanding more of this kind of functionality.
  Consumers want their devices to act like more than simple peripherals;
  they demand dual-role functionality. This presentation will be given by
  Matt Myers, technical contributor to the Certified Wireless USB
  Specification. Matt is also the lead Research and Development architect
  for Certified Wireless USB at Synopsys. He will be presenting at 8am on
  Thursday, June 22, 2006.

For more information on Certified Wireless USB DevCon, please visit:

http://www.usb.org/developers/events/Cert_WUSB_DevCon_June.

About Synopsys

Synopsys, Inc. is a world leader in electronic design automation (EDA) software for semiconductor design. The company delivers technology-leading semiconductor design and verification platforms and IC manufacturing software products to the global electronics market, enabling the development and production of complex systems-on-chips (SoCs). Synopsys also provides intellectual property and design services to simplify the design process and accelerate time-to-market for its customers. Synopsys is headquartered in Mountain View, California and has offices in more than 60 locations throughout North America, Europe, Japan and Asia. Visit Synopsys online at http://www.synopsys.com/.

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