

Synopsys Joins Si2

PRNewswire-FirstCall

DESIGN AUTOMATION CONFERENCE, ANAHEIM, Calif.

Synopsys, Inc. (NASDAQ: SNPS), a world leader in semiconductor design software, today announced it is joining Si2 as a result of the company's increased presence in analog simulation. As a member of Si2, Synopsys will advocate the migration of analog design to OpenAccess. Synopsys is also making available to Si2 its open source Liberty™ CCS model.

"It is the mark of a healthy industry when competing suppliers and customers alike can work together to solve shared challenges that enable industry growth," said Steven E. Schulz, President and CEO of Si2. "Synopsys' leadership and strong history of fostering openness is a welcome addition to Si2 and will help us to better tackle shared design flow and modeling needs."

"Joining Si2 at this juncture is a natural extension of Synopsys' continued push to offer end users the best design solutions possible," said Rich Goldman, vice president, Strategic Market Development, Synopsys Inc. "Si2 has been making significant strides to serve the entire electronic design community. In recent years, Si2 has adapted systems and structures more in line with industry needs. Synopsys is now working with Si2 to determine the significant role it will play in the standards organization."

About Synopsys

Synopsys, Inc. is a world leader in electronic design automation (EDA) software for semiconductor design. The company delivers technology-leading semiconductor design and verification platforms and IC manufacturing software products to the global electronics market, enabling the development and production of complex systems-on-chips (SoCs). Synopsys also provides intellectual property and design services to simplify the design process and accelerate time-to-market for its customers. Synopsys is headquartered in Mountain View, California, and has more than 60 offices located throughout North America, Europe, Japan and Asia. Visit Synopsys online at <http://www.synopsys.com/>.

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