Synopsys Expands University Program by Donating Technology Licenses to Southern Methodist University

SMU to Use Synopsys Tools for Industrial Research in Embedded System Design

PRNewswire-FirstCall MOUNTAIN VIEW, Calif.

Synopsys, Inc. (NASDAQ: SNPS), a world leader in semiconductor design software, today announced that it has donated technology licenses spanning a broad range of Synopsys products, including its Galaxy™ design and Discovery™ verification platforms, to Southern Methodist University (SMU). SMU will use the licenses for industrial research in embedded system design, as well as for general electronic design automation (EDA) education.

"We greatly appreciate the donation of the Synopsys tool suite to SMU," said Steve Szygenda, Cecil H. Green professor of engineering at SMU. "This will help us continue to be a leader in the EDA research and education fields."

"In our proposed research, we are developing techniques for modeling embedded systems at the concept level using Synopsys tools," said Mitch Thornton, professor of engineering at SMU and leader of the industrial research efforts. "For this program to work, it is crucial that we couple our models with Synopsys' state-of-theart toolsets. This donation will allow us to do so."

"Furthering math and science education around the world is a fundamental value at Synopsys," said Aart de Geus, chairman and chief executive officer at Synopsys and a graduate of SMU. "To that end, we will bring a full spectrum of the world's most powerful EDA tools to SMU for education and research. Along with tool donations, we provide universities with additional curriculum modules, training for professors and classroom support. We look forward to working with SMU and the high-tech university community as we partner in developing an engineering work force for the future."

Established in 1986, Synopsys' University Programs provide software, curricula and support to electrical engineering and computer science departments in universities around the world. Universities receive these benefits for instructional purposes and academic research projects at a modest cost. More information about Synopsys' university programs can be found at: https://www.synopsys.com/community/university-program.html

About Synopsys

Synopsys, Inc. is a world leader in EDA software for semiconductor design. The company delivers technology-leading semiconductor design and verification platforms and IC manufacturing software products to the global electronics market, enabling the development and production of complex systems-on-chips (SoCs). Synopsys also provides intellectual property and design services to simplify the design process and accelerate time-to-market for its customers. Synopsys is headquartered in Mountain View, California and has offices in more than 60 locations throughout North America, Europe, Japan and Asia. Visit Synopsys online at http://www.synopsys.com/.

NOTE: Synopsys is a registered trademark of Synopsys, Inc. Galaxy and Discovery are trademarks of Synopsys. All other trademarks or registered trademarks mentioned in this release are the intellectual property of their respective owners.

CONTACT: Pierre Golde of Synopsys, Inc., +1-650-584-4194, or golde@synopsys.com; or Melissa Chanslor of Edelman Public Relations, +1-650-429-2797, or melissa.chanslor@edelman.com, for Synopsys.

SOURCE: Synopsys, Inc.

CONTACT: Pierre Golde of Synopsys, Inc., +1-650-584-4194, or golde@synopsys.com; or Melissa Chanslor of Edelman Public Relations, +1-650-429-2797, or melissa.chanslor@edelman.com, for Synopsys

Web site: http://www.synopsys.com/