Global Unichip Achieves Gigahertz+ Frequency on ARM Cortex-A9 Processor with Synopsys IC Compiler

Standardizes on IC Compiler for High-Performance Processor Implementation

MOUNTAIN VIEW, Calif., Dec. 14, 2011 /PRNewswire/ -- Synopsys, Inc. (Nasdaq: SNPS), a world leader in software and IP used in the design, verification and manufacture of electronic components and systems, and Global Unichip Corporation (GUC; TW: 3443), the Flexible ASIC Leader™, today announced that GUC has achieved more than one gigahertz frequency on a dual-core ARM® Cortex™-A9 MPCore™ processor with Synopsys IC Compiler, a key component of Synopsys' Galaxy™ Implementation Platform. Synopsys' high-performance Galaxy implementation methodology was instrumental in achieving more than one gigahertz frequency with minimum power, while reducing schedule risk.

"As the Flexible ASIC Leader, we serve the highly competitive, smart electronics market," said Jim Lai, president, GUC. "For our customers, performance, power and time to market are key differentiators. Partnering with Synopsys to combine their leading-edge tools and technologies with our advanced process and low power design expertise has enabled us to strengthen our service offering and address customer demands."

"We faced several challenges to meet the frequency target for our high-end processor core based designs, which motivated us to adopt IC Compiler," said Albert Li, director, Design Development, Design Service Division, GUC. "Along with Design Compiler® Topographical, IC Compiler's design closure capabilities were critical in closing the frequency gap and helping us tapeout on time. We have standardized this flow for our 40- and 28- nanometer core hardening needs."

The five-million-gate, dual-core ARM Cortex-A9 processor, intended for high-end digital television chips, was fabricated on a TSMC 40nm low power process. It achieved a signoff frequency of 1 GHz at the worst process corner and 1.3 GHz at the typical process corner, without requiring the use of overdrive voltage. GUC used the Synopsys Galaxy implementation methodology to overcome the design challenges associated with achieving this level of operating frequency and power, including:

- Sensitivity of high-performance designs to memory macro placement, making it difficult to meet timing between the memories and processors
- Placement of register banks for improved frequency and routability, often requiring support for structured placement techniques
- High utilization in excess of 80 percent, requiring timing and congestion to be managed from the outset, starting with synthesis through place and route
- Tight skew and latency requirements for clock distribution network

Highlights of GUC's Galaxy implementation flow include:

- Design Compiler Topographical to create a better initial netlist for IC Compiler physical implementation
- IC Compiler's design planning technology for macro placement, along with its physical datapath technology for optimal placement of register banks
- PrimeTime® for tight correlation between implementation and signoff static timing analysis to deliver high-performance, low power, correlated results

"Synopsys IC Compiler has been the widely recognized tool of choice for high-performance processor design," said Antun Domic, senior vice president and general manager, Implementation Group at Synopsys. "We have continued to invest in optimization technology delivering high operating frequency while consuming the lowest power. Our collaboration with GUC and resulting tapeout success at gigahertz-plus speed is strong evidence of our technology delivering winning results."

About GUC

GLOBAL UNICHIP CORP. (GUC) is the Flexible ASIC Leader™ whose customers target IC devices to leading edge computing, communications and consumer applications. Based in Hsin-chu, Taiwan GUC has developed a global reputation with a presence in China, Europe, Japan, Korea, and North America. GUC is publicly traded on the Taiwan Stock Exchange under the symbol 3443. For more information, go to www.globalunichip.com

About Synopsys

Synopsys, Inc. (Nasdaq: SNPS) is a world leader in electronic design automation (EDA), supplying the global electronics market with the software, intellectual property (IP) and services used in semiconductor design, verification and manufacturing. Synopsys' comprehensive, integrated portfolio of implementation, verification, IP, manufacturing and field-programmable gate array (FPGA) solutions helps address the key challenges designers and manufacturers face today, such as power and yield management, system-to-silicon verification and time-to-results. These technology-leading solutions help give Synopsys customers a competitive edge in bringing the best products to market quickly while reducing costs and schedule risk. Synopsys is headquartered in Mountain View, California, and has approximately 70 offices located throughout North America, Europe, Japan, Asia and India. Visit Synopsys online at http://www.synopsys.com/**.**

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