eSilicon Selects Synopsys' Custom IC Design Solution and Tapes Out 28-nm Designs

Comprehensive Solution Enables Rapid Ramp-up and Delivery of Advanced Custom IP

MOUNTAIN VIEW, Calif., Oct. 25, 2011 /PRNewswire/ -- Synopsys, Inc. (Nasdaq: SNPS), a world leader in software and IP used in the design, verification and manufacture of electronic components and systems, today announced that eSilicon Corp. has deployed Synopsys' custom IC design solution to streamline its custom memory intellectual property (IP) business. Synopsys' comprehensive unified custom implementation solution, readily available interoperable process design kits (iPDKs) from leading foundries, and expert application support enabled eSilicon to rapidly deploy the solution and achieve multiple successful 28-nanometer (nm) tapeouts.

Custom memory IP developed by eSilicon uses complex, state-of-the-art memory architectures—including cache, content-addressable memory (CAM), SRAM, ROM, and multi-port register file (MPRF) IP—that must be created using a custom design flow to meet increasing performance and density constraints.

eSilicon successfully adopted the complete Synopsys custom solution, including the Galaxy Custom Designer® schematic and layout editor, HSPICE® and CustomSim™ circuit simulators, IC Validator design rule checking and layout versus schematic (DRC/LVS) solution, StarRC™ extraction tool, and PrimeYield foundry-qualified manufacturing compliance tool for final physical signoff.

"To support our custom memory IP business, we needed a complete, integrated custom IC design tool suite that could be rapidly adopted and deployed," said Patrick Soheili, vice president of marketing and business development and general manager of IP solutions at eSilicon. "The unified custom IC design solution from Synopsys, including Custom Designer, provides the productivity and performance our engineers need to quickly design and tape out IP at the 28-nanometer node."

"Custom Designer continues to gain adoption because of the breadth and depth of Synopsys' overall custom solution," said Bijan Kiani, vice president of product marketing at Synopsys. "The availability of iPDKs at advanced process nodes, combined with the productivity of our unified custom solution, allows designers to choose Synopsys' custom implementation solution to achieve higher productivity in an open, standards-based environment."

About Synopsys

Synopsys, Inc. (Nasdaq: SNPS) is a world leader in electronic design automation (EDA), supplying the global electronics market with the software, intellectual property (IP) and services used in semiconductor design, verification and manufacturing. Synopsys' comprehensive, integrated portfolio of implementation, verification, IP, manufacturing and field-programmable gate array (FPGA) solutions helps address the key challenges designers and manufacturers face today, such as power and yield management, system-to-silicon verification and time-to-results. These technology-leading solutions help give Synopsys customers a competitive edge in bringing the best products to market quickly while reducing costs and schedule risk. Synopsys is headquartered in Mountain View, California, and has approximately 70 offices located throughout North America, Europe, Japan, Asia and India. Visit Synopsys online at http://www.synopsys.com/.

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