

Sunplus Selects Synopsys as Its Primary EDA Partner

PRNewswire
MOUNTAIN VIEW, Calif.
(NASDAQ-NMS:SNPS)

MOUNTAIN VIEW, Calif., Sept. 15 /PRNewswire-FirstCall/ -- Synopsys, Inc. (NASDAQ: SNPS), a world leader in software and IP for semiconductor design, verification and manufacturing, today announced that Sunplus Technology Co., Ltd, a leading provider of advanced IC solutions for home entertainment applications, has signed an expanded business agreement to establish Synopsys as its primary EDA partner. Under the new multi-year agreement, Sunplus has consolidated on Synopsys' Galaxy™ Implementation and Discovery™ Verification Platforms for their chip development and design flows, and has extended their use of Synopsys DesignWare® IP.

"As the complexity of our designs has escalated to meet the demands of the rapidly evolving home entertainment markets, Synopsys has proven the reliable choice to help us meet those demands through a combination of tools, IP and services," said Ying-Chih Yang, chief technology officer, Home Entertainment Business Unit of Sunplus. "Using Synopsys products and services on our leading chips enables us to improve our design efficiency and focus on commercializing technologies for easy living."

With this expanded agreement, Sunplus has chosen products across Synopsys' broad portfolio, including the Galaxy Implementation Platform's IC Compiler place-and-route technology, Design Compiler® Ultra synthesis, Formality® power-aware equivalence checking, DFT Compiler, PrimeTime® SI signal integrity analysis, PrimeTime PX power analysis, StarRC™ parasitic extraction, DFTMAX™ compression, TetraMAX® automatic test pattern generation and TetraMAX DSMTTest; the Discovery Verification Platform's VCS® and HSPICE® simulators for analog and digital verification; and DesignWare Library's portfolio of synthesizable and verification IP.

"In consumer markets as dynamic as the segments that Sunplus serves, flexibility and predictability are critical success factors in the design process and Sunplus has demonstrated both," said John Chilton, Synopsys' senior vice president of marketing and strategic development. "Looking to the future, our broadened collaboration serves two key objectives for Sunplus: it enables them to continue to deliver differentiated solutions to their customers, and at the same time advance the robustness of their internal design methodologies and flows to support the development of new products."

About Synopsys

Synopsys, Inc. (NASDAQ: SNPS) is a world leader in electronic design automation (EDA), supplying the global electronics market with the software, intellectual property (IP) and services used in semiconductor design, verification and manufacturing. Synopsys' comprehensive, integrated portfolio of implementation, verification, IP, manufacturing and field-programmable gate array (FPGA) solutions helps address the key challenges designers and manufacturers face today, such as power and yield management, software-to-silicon verification and time-to-results. These technology-leading solutions help give Synopsys customers a competitive edge in bringing the best products to market quickly while reducing costs and schedule risk. Synopsys is headquartered in Mountain View, California, and has more than 65 offices located throughout North America, Europe, Japan, Asia and India. Visit Synopsys online at <http://www.synopsys.com/>.

Synopsys, Design Compiler, DesignWare, DFTMAX, Discovery, Formality, Galaxy, HSPICE, PrimeTime, StarRC,

TetraMAX and VCS are trademarks or registered trademarks of Synopsys, Inc. Any other trademarks or registered trademarks mentioned in this release are the intellectual property of their respective owners.

Editorial Contact:

Sheryl Gulizia

Synopsys, Inc.

650-584-8635

sgulizia@synopsys.com

Investor Contact:

Lisa Ewbank

Synopsys, Inc.

650-584-1901

SOURCE Synopsys, Inc.

SOURCE: Synopsys, Inc.

Web site: <http://www.synopsys.com/>
