

# Synopsys Delivers Comprehensive HDMI IP Solution for 90-nm to 40-nm Process Technologies

Connectivity IP Leader Broadens DesignWare IP Portfolio with Silicon-Proven, Compliant HDMI Transmitter and Receiver Controllers and PHY IP

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MOUNTAIN VIEW, Calif., Aug. 13 /PRNewswire-FirstCall/ -- Synopsys, Inc. (NASDAQ: SNPS), a world leader in software and IP for semiconductor design, verification and manufacturing, today announced the broad availability of silicon-proven High-Definition Multimedia Interface™ (HDMI™) transmitter and receiver digital controllers and PHY IP solutions as part of Synopsys' DesignWare IP portfolio. Synopsys' [DesignWare® IP for the HDMI](#) interface is compliant to the standard specification and supports High-bandwidth Digital Content Protection (HDCP). Synopsys also provides a roadmap for HDMI 1.4 with product availability anticipated at the end of 2009. The IP is available in leading process technologies from 90-nm down to 40-nm, and includes sample software drivers for system development. Synopsys' HDMI IP solution is differentiated in terms of feature set, available process nodes and worldwide technical support.

The DesignWare HDMI IP offering includes a comprehensive set of IP deliverables that helps designers quickly embed this complex interface into next generation multimedia SoCs with less risk. Furthermore, the DesignWare HDMI IP solution provides differentiated features including:

- Superior analog front end to support longer HDMI cables, while maintaining high performance
- Configurable RTL allow designers to optimize gate count and power consumption by choosing only the features required in their application
- Built-in (on-die) termination resistors for lower bill-of-materials
- Numerous optional features as well as a choice of various video and audio interfaces for ease of integration

"HDMI is one of fastest growing multimedia interfaces and is gaining wide-spread acceptance as the standard for communicating between high-performance digital devices," said Paul O'Donovan, principal analyst at Gartner. "Our research indicates exponential growth in the number of HDMI ports for mobile/portable markets such as camcorders and cameras. HDMI IP will play a key role in enabling designers to integrate the HDMI interface in their next-generation systems."

"Valens continues to develop next generation products for the high definition home entertainment market, and we chose Synopsys to provide us with a high-quality, compliant HDMI IP solution that would help us quickly implement the latest functionality into our advanced SoCs," said Massad Eyal, vice president of research and development R&D at Valens Semiconductor. "Synopsys' silicon-proven DesignWare HDMI IP enables us to deliver compelling products to the market faster and with significantly less risk."

"HDMI has emerged as the de-facto multimedia interface standard for high-definition consumer electronics and mobile devices," said John Koeter, vice president of marketing for the Solutions Group. "With Synopsys' extensive customer base, established sales and support infrastructure, and vast porting capabilities, designers can turn to a trusted HDMI IP solution that can be integrated into their SoC with less risk and improved time-to-market."

## Availability

The DesignWare HDMI 1.3 Transmit and Receive Controllers and PHY IP are available now in leading 90-nm, 65-nm, 45-nm, and 40-nm process technologies. The DesignWare HDMI 1.4 IP solution is currently in development and anticipated to be available at the end of 2009. For more information DesignWare HDMI IP, please visit: [www.synopsys.com/hdmi](http://www.synopsys.com/hdmi)

## About DesignWare IP

Synopsys is a leading provider of high-quality, silicon-proven interface and analog IP solutions for system-on-chip designs. Synopsys' broad IP portfolio delivers complete connectivity IP solutions consisting of controllers, PHY and verification IP for widely used protocols such as USB, PCI Express, DDR, SATA, HDMI and Ethernet. The analog IP family includes Analog-to-Digital Converters, Digital-to-Analog Converters, Audio Codecs, Video Analog Front Ends, Touch Screen Controllers and more. In addition, Synopsys offers SystemC transaction-level models to build virtual platforms for rapid, pre-silicon software development. With a robust IP development methodology, extensive investment in quality and comprehensive technical support, Synopsys enables designers to accelerate time-to-market and reduce integration risk. For more information on DesignWare IP, visit: <http://www.synopsys.com/designware>.

Follow us on Twitter at [http://twitter.com/designware\\_ip](http://twitter.com/designware_ip).

## About Synopsys

Synopsys, Inc. (NASDAQ: SNPS) is a world leader in electronic design automation (EDA), supplying the global electronics market with the software, intellectual property (IP) and services used in semiconductor design, verification and manufacturing. Synopsys' comprehensive, integrated portfolio of implementation, verification, IP, manufacturing and field-programmable gate array (FPGA) solutions helps address the key challenges designers and manufacturers face today, such as power and yield management, software-to-silicon verification and time-to-results. These technology-leading solutions help give Synopsys customers a competitive edge in bringing the best products to market quickly while reducing costs and schedule risk. Synopsys is headquartered in Mountain View, California, and has more than 65 offices located throughout North America, Europe, Japan, Asia and India. Visit Synopsys online at <http://www.synopsys.com/>.

## Forward-Looking Statement

This press release contains forward-looking statements within the meaning of Section 27A of the Securities Act of 1933 and Section 21E of the Securities Exchange Act of 1934, including Synopsys' expectations of the benefits and availability of the DesignWare HDMI 1.4 IP solution. These statements are based on current expectations and beliefs. Actual results could differ materially from these statements due to risks and uncertainties including, but not limited to, engineering difficulties, unforeseen difficulties in completing the commercial release of the solution and other risks as identified in the section of Synopsys' quarterly report on Form 10-Q for the fiscal quarter ended April 30, 2009, titled "Risk Factors." Statements included in this release are based upon information known to Synopsys as of the date of this release, and Synopsys assumes no obligation to publicly revise or update any forward-looking statement for any reason.

\* In-Stat 2009

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