

Advisory/Synopsys EDA Interoperability Forum to Feature Mike Santarini Keynote on Interoperability and the Future of EDA

Topics include 3rd party EDA tool integration with Galaxy Custom Designer™ solution and IPL Alliance Updates

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MOUNTAIN VIEW, Calif., Nov. 3 /PRNewswire-FirstCall/ -- Synopsys, Inc. (NASDAQ: SNPS), a world leader in software and IP for semiconductor design and manufacturing, announces that its 21st electronic design automation (EDA) Interoperability Forum will feature keynote speaker Mike Santarini, former EDA editor with EE Times and EDN Magazine, currently senior manager of Editorial services at Xilinx. His topic, "EDA is an Ecosystem: Interoperability and the Future of EDA."

WHO: The event is recommended for EDA tool developers, IC design engineers and IP providers to discuss the industry-critical topic of interoperability.

WHAT: The November 2008 Forum focuses on the latest developments in EDA interoperability and standards including:

- A Galaxy Custom Designer™ presentation by a Synopsys technical expert highlighting its open and standards-based environment, easy 3rd party tool integration and a high degree of interoperability.
- ClioSoft will talk about their experience integrating with Custom Designer.
- The latest update from the IPL Alliance on progress towards an interoperable PDK standard.
- The latest developments in SystemC TLM-2.0 and its benefits to the system design community
Hear how new innovations in the Liberty modeling standard enable the most important semiconductor design flows.
- A session on VMM for verification including a presentation on generating VMM compliant environments by Paradigm-Works.

WHEN: Thursday, November 6th in Santa Clara, Calif. and is open to all who wish to attend at no cost.

WHERE: The Sun Conference Center at Agnews Historic Park in Santa Clara, Calif. from 9:30am to 4:30pm. For more information, directions, and to register, visit:

<https://www.synopsys.com/news/events/devforums/2008/nov/index.html>

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