Synopsys Announces Earnings Release Date and Conference Call for Fourth Quarter and Fiscal Year 2007

PRNewswire-FirstCall MOUNTAIN VIEW, Calif. (NASDAQ:SNPS)

MOUNTAIN VIEW, Calif., Nov. 6 /PRNewswire-FirstCall/ -- Synopsys, Inc. (NASDAQ: SNPS), world leader in software and IP for semiconductor design and manufacturing, today announced it will report results for the fourth quarter and fiscal year 2007 on Thursday, December 6, 2007, after the market close.

A conference call to review the results will begin at 2 p.m. PST (5 p.m. EST) and will be hosted by Aart de Geus, chairman and chief executive officer, and Brian Beattie, chief financial officer.

Financial and other statistical information to be discussed on this conference call will be available on the corporate website at http://www.synopsys.com/corporate/invest/invest.html immediately before the call. A live webcast will also be available on this site. Participants should access the live webcast at least 10 minutes prior to the start of the call. A webcast replay can be accessed on the corporate web site beginning Thursday, December 6, 2007, at approximately 5:30 p.m. PST. The replay will remain available until Synopsys announces its first quarter fiscal 2008 earnings results in February, 2008. In addition, a dial-up replay of the conference call will be available beginning December 6, 2007 at 5:30 p.m. PST, ending on December 20, 2007. The replay telephone number is USA (800) 475-6701, and International (320) 365-3844, Access Code: 893911.

About Synopsys

Synopsys, Inc. (NASDAQ: SNPS) is a world leader in electronic design automation (EDA) software for semiconductor design. The company delivers technology-leading system and semiconductor design and verification platforms, IC manufacturing and yield optimization solutions, semiconductor intellectual property and design services to the global electronics market. These solutions enable the development and production of complex integrated circuits and electronic systems. Through its comprehensive solutions, Synopsys addresses the key challenges designers and manufacturers face today, including power management, accelerated time to yield and system-to-silicon verification. Synopsys is headquartered in Mountain View, California, and has more than 60 offices located throughout North America, Europe, Japan and Asia. Visit Synopsys online at http://www.synopsys.com/.

Synopsys is a registered trademark of Synopsys, Inc. All other trademarks mentioned in this release are the intellectual property of their respective owners.

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