/C O R R E C T I O N - Synopsys, Inc./

PRNewswire

In the news release, NVIDIA Delivers Most Advanced Graphics Processor Ever, Designed Using Synopsys' Galaxy Design Platform, issued earlier today by Synopsys, Inc. over PR Newswire, we are advised by the company that, in paragraph one, the third sentence should read, "The GeForce 6800 was designed using Synopsys Galaxy Design Platform products, including Design Compiler® (DC) Ultra, Power Compiler™, DFT Compiler™ SoCBIST, Module Compiler™, DesignWare® Library, Physical Compiler®, Astro™, PrimeTime®, Star-RCXT™, TetraMAX® and Hercules™, and was implemented in 0.13 micron IBM silicon," rather than, "The GeForce 6800 was designed using Synopsys Galaxy Design Platform products, including Design Compiler® (DC) Ultra, Power Compiler™, Module Compiler™, Physical Compiler®, Astro™, PrimeTime®, Star-RCXT™ and Hercules™, and was implemented in 0.13 micron IBM silicon."

And in the trademark note, the first sentence should read, "Synopsys, the Synopsys logo, Design Compiler, Physical Compiler, PrimeTime, TetraMAX, DesignWare and VCS are registered trademarks of Synopsys, Inc., and Galaxy, Discovery, Astro, Module Compiler, Power Compiler, DFT Compiler, Star-RCXT and Hercules are trademarks of Synopsys, Inc.," rather than, "Synopsys, the Synopsys logo, Design Compiler, Physical Compiler, PrimeTime and VCS are registered trademarks of Synopsys, Inc., and Galaxy, Discovery, Astro, Module Compiler, Power Compiler, Star-RCXT and Hercules are trademarks of Synopsys, Inc.," as originally issued inadvertently.

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