

Chipidea Licenses Synopsys' Galaxy Design and Discovery Verification Platforms

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LISBON, Portugal and MOUNTAIN VIEW, Calif.

Synopsys, Inc. (NASDAQ: SNPS), a world leader in semiconductor design software, and Chipidea, a world-leading analog and mixed-signal semiconductor IP provider, today announced that Chipidea has licensed key tools included in Synopsys' Galaxy™ Design and Discovery™ Verification platforms to address tough IP design challenges.

Chipidea demands leading EDA solutions to design and verify its complex IP. For this, Chipidea selected Synopsys' Galaxy Design Platform, which permits a range of design functions including RTL synthesis, physical implementation, test automation, signal integrity closure, parasitic extraction and physical verification. Chipidea will also have access to Synopsys' Discovery Verification Platform, including the VCS® tool for RTL verification, and HSPICE® and NanoSim® tools for mixed-signal design and verification.

"As a world-leading provider of analog and mixed-signal IP solutions, Chipidea is committed to engineering excellence and to providing its customers with a broad, state-of-the-art, proven IP portfolio," said Miguel Sampaio, General Manager of Chipidea Microelectronica Digital Technology Center and vice president of IP Connectivity Solutions. "We believe our adoption of Synopsys technology provides what we need to complement our expertise."

"Chipidea's use of our implementation and verification technology as their primary design flow demonstrates the high value of Synopsys' solutions in solving complicated design challenges," said Bijan Kiani, vice president of marketing for Synopsys' Implementation Group. "Synopsys is committed to delivering best-in-class technology to companies like Chipidea to raise the quality of their design environments and increase overall customer success."

About Chipidea Microelectronica, SA

Chipidea is a world-leading analog and mixed-signal semiconductor IP company headquartered in Portugal providing solutions for complex connectivity and processing applications. Chipidea works together with all major open foundries as well as captive fabs from global semiconductor firms. Its broad portfolio ranges from AD/DA converters to Codecs, Power and Clock Management, AFEs for digital media and communications, wireless transceivers, and USB physical interfaces. Chipidea employs over 160 people worldwide, with plans to reach 250 in 2006, and has offices in Europe, North America and Asia. Chipidea can be found on the web at www.chipidea.com.

About Synopsys

Synopsys, Inc. is a world leader in electronic design automation (EDA) software for semiconductor design. The company delivers technology-leading semiconductor design and verification platforms and IC manufacturing software products to the global electronics market, enabling the development and production of complex systems-on-chips (SoCs). Synopsys also provides intellectual property and design services to simplify the design process and accelerate time-to-market for its customers. Synopsys is headquartered in Mountain View, California, and has more than 60 offices located throughout North America, Europe, Japan and Asia. Visit Synopsys online at www.synopsys.com.

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SOURCE: Synopsys, Inc.

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