

Synopsys Launches Galaxy Power Seminars Worldwide

Technical Seminar Shows Users How to Achieve 2X Power Reduction and Power Integrity Sign-off

PRNewswire-FirstCall
MOUNTAIN VIEW, Calif.

Synopsys, Inc. (NASDAQ: SNPS), the world leader in semiconductor design software, today announced the continuation of its Galaxy™ Power seminar series. The technical sessions will demonstrate how to address today's toughest power challenges using new capabilities in Synopsys' Galaxy™ Power solution, including a 2X dynamic and 2X-to-10X leakage power reduction, power network synthesis in design planning, power-aware test synthesis, and ATPG, power analysis, and power integrity sign-off.

"Power management is now mission-critical to our customers' designs," said Antun Domic, senior vice president and general manager of Synopsys' Implementation Group. "Galaxy Power offers the industry's only comprehensive low-power solution today. Our seminars show designers how to use Galaxy Power to their full advantage to realize power-efficient designs."

The Galaxy Power seminar series was successfully launched on April 6 in Tokyo, Japan with a broad range of executive-level and technical attendees and quickly expanded to Europe. The next series of seminars will take place throughout Asia and the USA in July and August, as follows:

July 14, 2004	Seoul, Korea
July 16, 2004	Hsinchu, Taiwan
July 20, 2004	Beijing, PRC
July 22, 2004	Bangalore, India
August 10, 2004	Marlborough, MA
August 12, 2004	Ottawa, CAN
August 17, 2004	San Diego, CA
August 19, 2004	Austin, TX
August 24, 2004	Sunnyvale, CA

Interested parties can learn more about the seminar sessions and register online at http://www.synopsys.com/news/events/seminars/power_sem.html.

About Synopsys

Synopsys, Inc. is the world leader in electronic design automation (EDA) software for semiconductor design. The company delivers technology-leading semiconductor design and verification platforms and IC manufacturing software products to the global electronics market, enabling the development and production of complex systems-on-chips (SoCs). Synopsys also provides intellectual property and design services to simplify the design process and accelerate time-to-market for its customers. Synopsys is headquartered in Mountain View, California, and has more than 60 offices located throughout North America, Europe, Japan and Asia. Visit Synopsys online at <http://www.synopsys.com/>.

NOTE: Synopsys is a registered trademark of Synopsys, Inc. Galaxy is a trademark of Synopsys. All other trademarks or registered trademarks mentioned in this release are the intellectual property of their respective owners.

SOURCE: Synopsys, Inc.

CONTACT: editorial, Nancy Renzullo of Synopsys, Inc., +1-650-584-1669, or renzullo@synopsys.com; or Sarah Seifert of Edelman Public Relations, +1-650-429-2776, or Sarah.seifert@edelman.com, for Synopsys, Inc.

Web site: <http://www.synopsys.com/>
