

Synopsys Awarded TSMC's Interface IP Partner of the Year

Technology Leadership and Outstanding Customer Support Cited as Key Selection Criteria

MOUNTAIN VIEW, Calif., Nov. 7, 2011 [PRNewswire/](#) -- Synopsys, Inc. (Nasdaq: SNPS), a world leader in software and IP used in the design, verification and manufacture of electronic components and systems, today announced that it received TSMC's 2011 Interface IP Partner of the Year Award. Synopsys was selected based on customer feedback, TSMC-9000 compliance, technical support excellence and number of customer tapeouts. Synopsys' DesignWare® Interface IP portfolio includes widely used protocols such as [USB](#), [DDR](#), [PCI Express](#), [HDMI](#), [MIPI](#) and [SATA](#) that are offered in a broad range of processes from 180-nanometer (nm) to 28-nm.

"Our selection of Synopsys is an endorsement of their successful TSMC-9000 compliance and commitment to excellent customer support," said Suk Lee, director of design infrastructure marketing at TSMC. "Our ongoing collaboration with Synopsys results in our mutual customers getting the IP they need to meet their SoC design goals."

"TSMC and Synopsys share a commitment to delivering high-performance, high-quality IP that helps designers mitigate their project risks, and Synopsys is honored to be recognized for this commitment for the second year in a row," said John Koeter, vice president of marketing for IP and systems at Synopsys. "With the extensive portfolio of DesignWare IP products that are silicon-proven across a broad spectrum of TSMC processes, designers can reduce their integration risk and focus on the design blocks that differentiate their chips."

About DesignWare IP

Synopsys is a leading provider of high-quality, silicon-proven IP solutions for SoC designs. The broad DesignWare IP portfolio includes complete [interface IP](#) solutions consisting of controllers, PHY and verification IP for widely used protocols, [analog IP](#), [embedded memories](#), [logic libraries](#) and [configurable processor cores](#). In addition, Synopsys offers [SystemC™ transaction-level models](#) to build virtual prototypes for rapid, pre-silicon development of software. With a robust IP development methodology, [reuse tools](#), extensive investment in quality and comprehensive technical support, Synopsys enables designers to accelerate time-to-market and reduce integration risk. For more information on DesignWare IP, visit: <https://www.synopsys.com/designware>. Follow us on Twitter at http://twitter.com/designware_ip.

About Synopsys

Synopsys, Inc. (Nasdaq:SNPS) is a world leader in electronic design automation (EDA), supplying the global electronics market with the software, intellectual property (IP) and services used in semiconductor design, verification and manufacturing. Synopsys' comprehensive, integrated portfolio of implementation, verification, IP, manufacturing and field-programmable gate array (FPGA) solutions helps address the key challenges designers and manufacturers face today, such as power and yield management, system-to-silicon verification and time-to-results. These technology-leading solutions help give Synopsys customers a competitive edge in bringing the best products to market quickly while reducing costs and schedule risk. Synopsys is headquartered in Mountain View, California, and has approximately 70 offices located throughout North America, Europe, Japan, Asia and India. Visit Synopsys online at <https://www.synopsys.com/>.

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