Synopsys Acquires Extreme DA

MOUNTAIN VIEW, Calif., Oct. 7, 2011 /PRNewswire/ -- Synopsys, Inc. (Nasdaq: SNPS), a world leader in software and IP used in the design, verification and manufacture of electronic components and systems, announced today that it has completed the acquisition of Extreme DA™, a privately held company headquartered in Santa Clara, California that develops software to improve integrated circuit (IC) design performance, power consumption and manufacturing yields. The acquisition extends Synopsys' expertise in static timing analysis and multicore software development by adding technology and engineering talent to accelerate advancements in Synopsys' timing analysis solutions. In addition to having access to technology that can help them address Giga-scale design challenges, customers will benefit from a world-class sales support team. The terms of the acquisition, which closed October 6, have not been disclosed.

"Synopsys is committed to helping designers of next-generation ICs meet the productivity and performance challenges created by the demand for smart electronics and mobile devices," said Antun Domic, senior vice president and general manager of Synopsys' Implementation Group. "The combination of Synopsys and Extreme DA's technological capabilities and team expertise will help enable Synopsys to deliver best-in-class tools that address the scalability, convergence and throughput needs of our customers' massive designs."

"We believe our customers will be pleased with this combination as it brings together the best of each of our respective product lines for the continued advancement of critical timing analysis capability in an era of exploding design complexity," said Mustafa Celik, Chief Executive Officer of Extreme DA."

Synopsys PrimeTime® timing analysis is recognized as the golden, trusted signoff solution for timing, signal integrity, power and variation-aware analysis. It helps pinpoint problems prior to tapeout, thereby reducing risk, ensuring design integrity and lowering the cost of design. In 2010, Synopsys introduced PrimeTime HyperScale technology, enabling static timing analysis (STA) to scale beyond 500 million instances.

Extreme DA is recognized for ushering in the first commercial statistical static timing analysis (SSTA) tool suite, and for later introducing the ability to statistically model on-chip variation without the need for expensive cell library characterization in its GoldTime™ static timing analysis tool.

About Synopsys

Synopsys, Inc. (Nasdaq:SNPS) is a world leader in electronic design automation (EDA), supplying the global electronics market with the software, intellectual property (IP) and services used in semiconductor design, verification and manufacturing. Synopsys' comprehensive, integrated portfolio of implementation, verification, IP, manufacturing and field-programmable gate array (FPGA) solutions helps address the key challenges designers and manufacturers face today, such as power and yield management, system-to-silicon verification and time-to-results. These technology-leading solutions help give Synopsys customers a competitive edge in bringing the best products to market quickly while reducing costs and schedule risk. Synopsys is headquartered in Mountain View, California, and has approximately 70 offices located throughout North America, Europe, Japan, Asia and India. Visit Synopsys online at http://www.synopsys.com.

Safe Harbor Statement

This press release contains forward-looking statements within the meaning of Section 27A of the Securities Act of 1933 and Section 21E of the Securities Exchange Act of 1934, including statements regarding customer benefits and the anticipated effect of the acquisition on Synopsys' timing analysis products. These statements are based on current expectations and beliefs. Actual results could differ materially from those described by these statements due to risks and uncertainties including, but not limited to, unforeseen production or delivery delays, failure to perform as expected, changes in demand, and other risks detailed in Synopsys' filings with the U.S. Securities and Exchange Commission, including those described in the "Risk Factors" section of the latest Quarterly Report on Form 10-Q for the fiscal quarter ended April 30, 2011.

Synopsys is a registered trademark of Synopsys, Inc. All other trademarks mentioned in this release are the intellectual property of their respective owners.

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