Synopsys and GLOBALFOUNDRIES Collaborate to Deliver Interoperable Process Design Kits (iPDKs)

Synopsys Custom Design Solution Now Supported by GLOBALFOUNDRIES 65nm Process Technologies

MOUNTAIN VIEW, Calif., July 11, 2011 /PRNewswire/ -- Synopsys, Inc. (Nasdaq: SNPS), a world leader in software and IP for semiconductor design, verification and manufacturing, today announced that it has collaborated with GLOBALFOUNDRIES to jointly develop, validate, support and distribute interoperable process design kits (iPDKs) for GLOBALFOUNDRIES' mainstream and advanced process technologies. The GLOBALFOUNDRIES 65-nanometer (nm) iPDKs are available now with additional iPDKs planned for delivery later in 2011. The iPDKs have been fully validated with Synopsys' custom design solution, including the Galaxy Custom Designer® tool, HSPICE® circuit simulator, CustomSim™ circuit simulator, IC Validator physical verification tool and StarRC™ extraction tool.

"The iPDK collaboration with Synopsys is a continuation of our design enablement initiative," said Henry Law, vice president of technology development at GLOBALFOUNDRIES. "With the delivery of iPDKs, we are able to address a broader market segment with our leading process technologies and give our customers more choices in adopting new EDA solutions such as Synopsys' Custom Designer."

"Interoperable PDKs accelerate and broaden designer access to new process nodes, promote design reuse and portability," said Paul Lo, senior vice president and general manager of the Analog/Mixed-Signal Group at Synopsys. "Through this iPDK collaboration, our support for GLOBALFOUNDRIES' process technology is now expanded to include Synopsys' custom design solution, in addition to what is already available for our digital implementation and IP solutions."

The GLOBALFOUNDRIES 65nm-G (generic process) and 65nm-LPe (enhanced low power process) iPDKs are available now. Please contact GLOBALFOUNDRIES for more information.

About Synopsys

Synopsys, Inc. (Nasdaq: SNPS) is a world leader in electronic design automation (EDA), supplying the global electronics market with the software, intellectual property (IP) and services used in semiconductor design, verification and manufacturing. Synopsys' comprehensive, integrated portfolio of implementation, verification, IP, manufacturing and field-programmable gate array (FPGA) solutions helps address the key challenges designers and manufacturers face today, such as power and yield management, system-to-silicon verification and time-to-results. These technology-leading solutions help give Synopsys customers a competitive edge in bringing the best products to market quickly while reducing costs and schedule risk. Synopsys is headquartered in Mountain View, California, and has approximately 70 offices located throughout North America, Europe, Japan, Asia and India. Visit Synopsys online at www.synopsys.com.

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Editorial Contacts:
Sheryl Gulizia
Synopsys, Inc.
650-584-8635
sgulizia@synopsys.com

Lisa Gillette-Martin
MCA, Inc.
650-968-8900 ext. 115
lgmartin@mcapr.com

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