

MEDIA ADVISORY/ALERT: Synopsys Hosts Special Events With Industry Leaders at DAC 2011

MOUNTAIN VIEW, Calif., June 1, 2011 /PRNewswire/ -- Synopsys, Inc. (Nasdaq: SNPS), a world leader in software and IP for semiconductor design, verification and manufacturing, will host several customer special events at DAC 2011 in San Diego, Calif. Attendees will get to hear panels of technical industry experts from leading companies speak about real-world semiconductor design, verification and foundry challenges and solutions.

Monday, June 6, 2011

WHAT: IC Compiler Luncheon

TOPIC: Gigascale Designs with IC Compiler

SPEAKERS: Juniper Networks, Lantiq, LSI, Panasonic, Renesas

TIME: 11:30 a.m. to 1:30 p.m.

LOCATION: San Diego Marriott, Marina Ballroom, Salon F

WHAT: IPL Alliance Luncheon

TOPIC: [Interoperable PDKs are Here to Stay: New Era of Analog/Custom Innovations](#)

SPEAKERS: TowerJazz, TSMC, STMicroelectronics, Xilinx

TIME: 11:30 a.m. to 1:30 p.m.

LOCATION: San Diego Marriott, Marina Ballroom, Salon D

WHAT: PrimeTime® Special Interest Group (SIG) Reception

TOPIC: Next-generation ECO Guidance Technology

SPEAKERS: Broadcom, Cisco Systems, GLOBALFOUNDRIES, LSI, STMicroelectronics

TIME: 6:00 p.m. to 8:30 p.m.

LOCATION: San Diego Marriott, Marina Ballroom, Salon F

WHAT: Synopsys/Dell University Reception

SPEAKERS: Jason Cong, UCLA; Kevin Thompson, Synopsys

TIME: 6:00 p.m. to 8:00 p.m.

LOCATION: San Diego Marriott, Marina Ballroom, Salon D

Tuesday, June 7, 2011

WHAT: ARM/GLOBALFOUNDRIES/Samsung/Synopsys Breakfast

TOPIC: 32nm/28nm Ready...All Aboard!

SPEAKERS: ARM, GLOBALFOUNDRIES, Samsung, Synopsys

TIME: 7:15 a.m. to 8:30 a.m.

LOCATION: San Diego Marriott, Marina Ballroom, Salon F

WHAT: Verification Luncheon

TOPIC: [Industry Leaders Verify with Synopsys](#)

SPEAKERS: Cisco, Freescale, MediaTek, Qualcomm, Renesas

TIME: 11:45 a.m. to 1:45 p.m.

LOCATION: San Diego Marriott, Marina Ballroom, Salon F

WHAT: AMS Verification Dinner

TOPIC: SPICE Up Your Chip: Achieving Fast, Accurate AMS Verification

SPEAKERS: AMD, Juniper Networks, nVidia, Qualcomm, Xilinx

TIME: 6:00 p.m. to 8:30 p.m.

LOCATION: San Diego Marriott, Marina Ballroom, Salon F

Wednesday, June 8, 2011

WHAT: Interoperability Breakfast

TOPIC: On Safari with Custom Design Interoperability & Interconnect Modeling Standards

SPEAKERS: GLOBALFOUNDRIES, LSI, STMicroelectronics, TowerJazz

TIME: 7:30 a.m. to 9:20 a.m.

LOCATION: San Diego Marriott, Marina Ballroom, Salon F

About Synopsys

Synopsys, Inc. (Nasdaq: SNPS) is a world leader in electronic design automation (EDA), supplying the global electronics market with the software, intellectual property (IP) and services used in semiconductor design, verification and manufacturing. Synopsys' comprehensive, integrated portfolio of implementation, verification, IP, manufacturing and field-programmable gate array (FPGA) solutions helps address the key challenges designers and manufacturers face today, such as power and yield management, system-to-silicon verification and time-to-results. These technology-leading solutions help give Synopsys customers a competitive edge in bringing the best products to market quickly while reducing costs and schedule risk. Synopsys is headquartered in Mountain View, California, and has approximately 70 offices located throughout North America, Europe, Japan, Asia and India. Visit Synopsys online at <http://www.synopsys.com/>.

Synopsys and PrimeTime are registered trademarks or trademarks of Synopsys, Inc. All other trademarks or registered trademarks mentioned in this release are the intellectual property of their respective owners.

Editorial Contacts:

Sheryl Gulizia
Synopsys, Inc.
650-584-8635
sgulizia@synopsys.com

Lisa Gillette-Martin
MCA, Inc.
650-968-8900 ext. 115
lgmartin@mcapr.com

SOURCE Synopsys, Inc.
