Synopsys to Present at 39th Annual Cowen TMT Conference

MOUNTAIN VIEW, Calif., May 25, 2011/PRNewswire/ -- Synopsys, Inc. (Nasdaq: SNPS), a world leader in software and IP for semiconductor design, verification and manufacturing, today announced that John Chilton, senior vice president of marketing and corporate development, will speak at the 39th Annual Cowen Technology, Media & Telecom Conference in New York, on June 2, 2011.

This event will be broadcast live on the Internet via the Synopsys corporate website at https://www.synopsys.com/Company/InvestorRelations on Thursday, June 2, 2011 at 9:30 a.m. ET (6:30 a.m. PT). To access the live webcast presentation, please go to the website at least ten minutes early to register and to download and install any necessary multimedia software. A webcast replay of the event can also be accessed at the Synopsys corporate website.

About Synopsys

Synopsys, Inc. (Nasdaq: SNPS) is a world leader in electronic design automation (EDA), supplying the global electronics market with the software, intellectual property (IP) and services used in semiconductor design, verification and manufacturing. Synopsys' comprehensive, integrated portfolio of implementation, verification, IP, manufacturing and field-programmable gate array (FPGA) solutions helps address the key challenges designers and manufacturers face today, such as power and yield management, system-to-silicon verification and time-to-results. These technology-leading solutions help give Synopsys customers a competitive edge in bringing the best products to market quickly while reducing costs and schedule risk. Synopsys is headquartered in Mountain View, California, and has approximately 70 offices located throughout North America, Europe, Japan, Asia and India. Visit Synopsys online at https://www.synopsys.com/.

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