Freescale Licenses Synopsys' DesignWare IP Portfolio to Accelerate SoC Designs

High-Quality, Broad Portfolio and Worldwide Technical Support Helps Freescale Speed SoC Development Time and Lower Risk

MOUNTAIN VIEW, Calif., March 23, 2011 /PRNewswire/ -- Synopsys, Inc. (Nasdaq: SNPS), a world leader in software and IP for semiconductor design, verification and manufacturing, today announced that Freescale Semiconductor, a global leader in the design and manufacture of semiconductors for the automotive, consumer, industrial and networking markets, has licensed Synopsys' DesignWare® IP for Freescale's system-on-chip (SoC) designs. The multi-year agreement gives Freescale access to Synopsys' broad portfolio of DesignWare interface and analog IP such as USB, HDMI, PCI Express®, SATA, and MIPI interfaces, as well as data converters, audio codecs and video front -ends. Synopsys' established leadership in delivering high-quality IP that supports the most widely used interfaces and advanced processes combined with an expert worldwide technical support team enables Freescale to reduce risk and speed time-to-market for their semiconductor designs.

"Access to Synopsys' very broad IP portfolio helps us keep pace with the complexities of new standard interfaces and migration to ultra-deep sub-micron processes," said Ken Hansen senior fellow, vice president and chief technology officer at Freescale Semiconductor. "Freescale has chosen Synopsys because of its robust architectures, protocol certification record, extensive silicon validation process and worldwide support teams that are located near our key design centers. This will enable our designers to focus their efforts on delivering differentiated products to the market."

"Synopsys' collaboration with Freescale has resulted in the successful development of SoCs for a range of applications, and we look forward to continuing this close relationship," said John Koeter, vice president of marketing for the Solutions Group at Synopsys. "As the industry's trusted IP partner, Synopsys provides a broad portfolio of silicon-proven, IP solutions that help customers such as Freescale integrate critical functionality into their SoCs with less risk and improved time-to-market."

About DesignWare IP

Synopsys is a leading provider of high-quality, silicon-proven IP solutions for SoC designs. The broad DesignWare® IP portfolio includes complete interface IP solutions consisting of controllers, PHY and Verification IP for widely used protocols, analog IP, embedded memories, logic libraries and configurable processor cores. In addition, Synopsys offers SystemC transaction-level models to build virtual prototypes for rapid, pre-silicon development of software. With a robust IP development methodology, reuse tools, extensive investment in quality and comprehensive technical support, Synopsys enables designers to accelerate time-to-market and reduce integration risk. For more information on DesignWare IP, visit: https://www.synopsys.com/designware. Follow us on Twitter at http://twitter.com/designware_ip.

About Synopsys

Synopsys, Inc. (Nasdaq: SNPS) is a world leader in electronic design automation (EDA), supplying the global electronics market with the software, intellectual property (IP) and services used in semiconductor design, verification and manufacturing. Synopsys' comprehensive, integrated portfolio of implementation, verification, IP, manufacturing and field-programmable gate array (FPGA) solutions helps address the key challenges designers and manufacturers face today, such as power and yield management, system-to-silicon verification and time-to-results. These technology-leading solutions help give Synopsys customers a competitive edge in bringing the best products to market quickly while reducing costs and schedule risk. Synopsys is headquartered in Mountain View, California, and has approximately 70 offices located throughout North America, Europe, Japan, Asia and India. Visit Synopsys online at https://www.synopsys.com/.

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