Wilocity Tapes-Out Multi-Gigabit Wireless Communication SoC Using Synopsys DesignWare IP

High-quality DesignWare IP Combined with Professional Services Speeds Development Time and Lowers Integration Risk for Wireless Gigabit Alliance SoC

MOUNTAIN VIEW, Calif., March 22, 2011 /PRNewswire/ -- Synopsys, Inc. (Nasdaq: SNPS), a world leader in software and IP for semiconductor design, verification and manufacturing, today announced that Wilocity has successfully taped-out its multi-gigabit Wireless Gigabit Alliance-compliant system-on-chip (SoC) utilizing Synopsys' broad portfolio of high-quality DesignWare® IP and assistance from Synopsys Professional Services. The DesignWare IP used in the SoC included PCI Express, embedded memories and logic libraries. Wilocity's SoC is based on the Wireless Gigabit Alliance (WiGig) and IEEE802.11ad draft specifications, enabling multi-gigabit wireless for a wide range of applications, from I/O to networking to video.

By leveraging the breadth of Synopsys' tools, DesignWare IP solutions and expertise from Synopsys Professional Services, Wilocity was able to reduce power consumption in some of the more complex arithmetic clusters by approximately 50 percent, increase performance by 65 percent and improve utilization by up to 70 percent compared to their previous design executed with a competitive solution.

"As consumers demand greater mobility, device manufacturers are requiring advanced mobile computing platforms that enable them to deliver thin and light devices without sacrificing performance and functionality," said Ido Naishtein, director of physical design at Wilocity. "Wilocity's use of proven DesignWare IP and close collaboration with Synopsys Professional Services has enabled us to successfully produce multi-gigabit wireless chips that are optimized for today's mobile devices."

Working in close cooperation, Wilocity and Synopsys designers made significant improvements in the architecture, partitioning and algorithm design. During the implementation phase, Wilocity leveraged tools from Synopsys' Galaxy™ Implementation Platform and the physical design experience of Synopsys Professional Services consultants to help them achieve their aggressive design targets and deliver a successful tape-out.

"Synopsys understands the importance of providing companies like Wilocity with high-quality IP as well as with expert design assistance to enable them to meet their design objectives and customer commitments," said John Koeter, vice president of marketing for the Solutions Group at Synopsys. "Wilocity's exceptional improvements in power, area and performance in its WiGig design are testaments to Synopsys' ability to provide comprehensive solutions of tools, IP and consulting that help our customers deliver differentiated products in the competitive mobile device market."

About DesignWare IP

Synopsys is a leading provider of high-quality, silicon-proven IP solutions for SoC designs. The broad DesignWare IP portfolio includes complete interface IP solutions consisting of controllers, PHY and Verification IP for widely used protocols,analog IP, embedded memories, logic libraries and configurable processor cores. In addition, Synopsys offersSystemC transaction-level models to build virtual prototypes for rapid, pre-silicon development of software. With a robust IP development methodology, reuse tools, extensive investment in quality and comprehensive technical support, Synopsys enables designers to accelerate time-to-market and reduce integration risk. For more information on DesignWare IP, visit: https://www.synopsys.com/designware. Follow us on Twitter athttp://twitter.com/designware_ip.

About Synopsys

Synopsys, Inc. (Nasdaq:SNPS) is a world leader in electronic design automation (EDA), supplying the global electronics market with the software, intellectual property (IP) and services used in semiconductor design, verification and manufacturing. Synopsys' comprehensive, integrated portfolio of implementation, verification, IP, manufacturing and field-programmable gate array (FPGA) solutions helps address the key challenges designers and manufacturers face today, such as power and yield management, system-to-silicon verification and time-to-results. These technology-leading solutions help give Synopsys customers a competitive edge in bringing the best products to market quickly while reducing costs and schedule risk. Synopsys is headquartered in Mountain View, California, and has approximately 70 offices located throughout North America, Europe, Japan, Asia and India. Visit Synopsys online at https://www.synopsys.com/.

Synopsys, DesignWare and Galaxy are registered trademarks or trademarks of Synopsys, Inc. Any other trademarks or registered trademarks mentioned in this release are the intellectual property of their respective owners.

Editorial Contacts:

Sheryl Gulizia
Synopsys, Inc.
650-584-8635
sgulizia@synopsys.com

Stephen Brennan
MCA
650-968-8900 x11
sbrennan@mcapr.com

SOURCE Synopsys, Inc.