Synopsys Announces Android Operating System Support for DesignWare ARC Processor Cores

Android Support for High-performance DesignWare ARC 750D Processor Addresses the Needs of Low Power, Cost-Sensitive Portable and Consumer Applications

Disclaimer: GlobalFoundries acquired the Synopsys Processor Solutions business on June 1, 2026. Clickhere to learn more.

MOUNTAIN VIEW, Calif., Jan. 12, 2011 /PRNewswire/ -- Synopsys, Inc. (Nasdaq: SNPS), a world leader in software and IP for semiconductor design, verification and manufacturing, today announced Android operating system (v2.2 FroYo) support for the DesignWare™ ARC™ 750D processor core. The DesignWare ARC 750D host processor, which runs at 1.1 GHz and delivers 1800 DMIPs in an industry standard 40-nanometer (nm) process, is an ideal choice for designers requiring a high-performance, low-power and cost-effective microprocessor to drive Android-based applications. The DesignWare ARC 750D processor now allows designers to leverage the extensive, existing software and applications available for the Android operating system. Furthermore, designers are able to configure the processor hardware and application software to differentiate their products in the market.

The DesignWare ARC 750D offers a broad range of processor, DSP and floating point capabilities, enabling designers to address a wide range of processing requirements with a single host application processor. Using a single processor simplifies the design, lowers silicon-area and enables faster debug of the chip. In addition, Synopsys offers designers a full suite of software and development tools to ease program development and debug. Shipped in more than 550 million ICs annually, Synopsys' high-quality DesignWare ARC processors can be integrated into system-on-chip (SoC) designs with less risk and improved time-to-market.

"Synopsys' DesignWare ARC 750D processor core with support for the Android operating system enabled us to easily integrate Android-based applications into our SoC early in the design process," said Robert Chen, president of AVID Electronics Corp. "By using Synopsys' configurable DesignWare ARC 750D processor, we are able to produce a high-performance, low-power solution that is helping deliver next-generation Android-based products reliably to the marketplace."

"The next-generation of consumer products requires support for very high-performance multimedia applications," said John Koeter, vice president of marketing for the Solutions Group at Synopsys. "The DesignWare ARC 750D with support for the Android OS, combined with our complementary audio and video subsystem solutions, gives designers a comprehensive IP solution for their multimedia application needs from a single, trusted IP provider."

Availability

The DesignWare ARC 750D processor is available now. Support for the Android operating system is available now for early adopters, and will be in general availability during Q3 2011. For more information about the Android platform and DesignWare ARC processor solutions, please visit https://www.synopsys.com/dw/ipdir.php?ds=arc_android.

About DesignWare IP

Synopsys is a leading provider of high-quality, silicon-proven IP solutions for SoC designs. The broad DesignWare IP portfolio includes complete interface IP solutions consisting of controllers, PHY and Verification IP for widely used protocols,analog IP, embedded memories, logic libraries, embedded test & repair IP and configurable processor cores. In addition, Synopsys offers SystemC™ transaction-level models to build virtual prototypes for rapid, pre-silicon development of software. With a robust IP development methodology, reuse tools, extensive investment in quality and comprehensive technical support, Synopsys enables designers to accelerate time-to-market and reduce integration risk. For more information on DesignWare IP, visit: https://www.synopsys.com/designware. Follow us on Twitter at http://twitter.com/designware_ip.

About Synopsys

Synopsys, Inc. (Nasdaq: SNPS) is a world leader in electronic design automation (EDA), supplying the global electronics market with the software, intellectual property (IP) and services used in semiconductor design, verification and manufacturing. Synopsys' comprehensive, integrated portfolio of implementation, verification, IP, manufacturing and field-programmable gate array (FPGA) solutions helps address the key challenges designers and manufacturers face today, such as power and yield management, system-to-silicon verification and time-to-results. These technology-leading solutions help give Synopsys customers a competitive edge in bringing the best products to market quickly while reducing costs and schedule risk. Synopsys is headquartered in Mountain View, California, and has approximately 70 offices located throughout North America, Europe, Japan, Asia and India. Visit Synopsys online at https://www.synopsys.com/.

Forward-Looking Statements

This press release contains forward-looking statements within the meaning of Section 27A of the Securities Act of 1933 and Section 21E of the Securities Exchange Act of 1934, including statements regarding the expected benefits of Synopsys' DesignWare ARC 750D processor core date of general availability of support for the Android operating system. These statements are based on current expectations and beliefs. Actual results could differ materially from these statements as a result of unforeseen engineering difficulties, uncertainties attendant to any new product offering, and certain statements contained in the section of Synopsys' Annual Report on Form 10-K for the fiscal year ended October 31, 2009, and subsequent forms 10-Q, entitled "Risk Factors."

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