

## Media Advisory/Alert: Synopsys Showcases DesignWare Sonic Focus, ARC Processor Cores and MIPI IP at CES 2011

LAS VEGAS, Jan. 6, 2011 [PRNewswire/](#) -- Synopsys, Inc. (Nasdaq: SNPS), a world leader in software and IP for semiconductor design, verification and manufacturing, will showcase its latest DesignWare® IP at [CES 2011](#) in Las Vegas, Nevada. See [live demonstrations](#) of the DesignWare ARC™ configurable processor cores, Sonic Focus® audio post-processing IP, and MIPI CSI-2 IP and speak with our product experts. As a trusted IP partner, Synopsys provides high-quality IP solutions that help reduce SoC integration risk, lower costs and speed time-to-market.

**WHAT:** Synopsys at CES 2011

**WHEN:** January 6-9, 2011

**WHERE:** The Venetian Hotel, Venetian Tower, Suites 30-334 and 30-336, Las Vegas, NV

**EXHIBIT HOURS:** 9:00 am to 6:00 pm

### DEMO DESCRIPTIONS:

- **DesignWare Sonic Focus Audio Post-Processing IP**

Experience a new generation of audio post-processing and enrichment technology, enabling high-quality and immersive audio for advanced consumer electronic multimedia products.

- **DesignWare ARC 32-Bit Configurable Processor Cores**

See the DesignWare ARC synthesizable processor cores utilizing the latest software stack to implement mobile and consumer products.

- **DesignWare MIPI CSI-2 Host Controller and D-PHY IP**

See streaming data from a standard MIPI-based camera to a display using the DesignWare MIPI CSI-2 Host controller and D-PHY IP.

For more information on Synopsys' participation at CES, please visit: <http://www.synopsys.com/IP/Pages/CES2011.aspx>

For more information on DesignWare IP, please visit: <http://www.synopsys.com/designware>.

### About Synopsys

Synopsys, Inc. (Nasdaq: SNPS) is a world leader in electronic design automation (EDA), supplying the global electronics market with the software, intellectual property (IP) and services used in semiconductor design, verification and manufacturing. Synopsys' comprehensive, integrated portfolio of implementation, verification, IP, manufacturing and field-programmable gate array (FPGA) solutions helps address the key challenges designers and manufacturers face today, such as power and yield management, system-to-silicon verification and time-to-results. These technology-leading solutions help give Synopsys customers a competitive edge in bringing the best products to market quickly while reducing costs and schedule risk. Synopsys is headquartered in Mountain View, California, and has approximately 70 offices located throughout North America, Europe, Japan, Asia and India. Visit Synopsys online at <http://www.synopsys.com/>.

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