Synopsys to Present at Barclays Capital 2010 Global Technology Conference

MOUNTAIN VIEW, Calif., Dec. 2, 2010 /PRNewswire/ -- Synopsys, Inc. (Nasdaq: SNPS), a world leader in software and IP for semiconductor design, verification and manufacturing, today announced that John Chilton, senior vice president of marketing and corporate development, will speak at the Barclays Capital 2010 Global Technology Conference in San Francisco, on December 9, 2010.

This event will be broadcast live over the Internet via the Synopsys corporate website at https://www.synopsys.com/Company/InvestorRelations on Thursday, December 9, 2010 at 3:00 p.m. PT (6:00 p.m. ET). To access the live audio webcast, please go to the website at least ten minutes early to register and to download and install any necessary multimedia software. The webcast replay of the presentation can be accessed at the Synopsys corporate website approximately four hours following the conclusion of the live event.

About Synopsys

Synopsys, Inc. (Nasdaq: SNPS) is a world leader in electronic design automation (EDA), supplying the global electronics market with the software, intellectual property (IP) and services used in semiconductor design, verification and manufacturing. Synopsys' comprehensive, integrated portfolio of implementation, verification, IP, manufacturing and field-programmable gate array (FPGA) solutions helps address the key challenges designers and manufacturers face today, such as power and yield management, system-to-silicon verification and time-to-results. These technology-leading solutions help give Synopsys customers a competitive edge in bringing the best products to market quickly while reducing costs and schedule risk. Synopsys is headquartered in Mountain View, California, and has approximately 70 offices located throughout North America, Europe, Japan, Asia and India. Visit Synopsys online at https://www.synopsys.com/.

Investor Contact:
Roberta Reid
Synopsys, Inc.
(650) 584-1901

SOURCE Synopsys, Inc.