eMemory Standardizes on Synopsys FastSPICE for Circuit Simulation

CustomSim's Superior Runtime and Accuracy Cited as Key Decision Criteria

MOUNTAIN VIEW, Calif., Sept. 14 /PRNewswire/ -- Synopsys, Inc. (Nasdaq: SNPS), a world leader in software and IP for semiconductor design, verification and manufacturing, today announced that eMemory Technology, Inc. has selected Synopsys' CustomSim™ solution for all of its circuit simulation needs. On a 45-nanometer (nm) embedded non-volatile memory, the CustomSim solution demonstrated up to 2 times faster simulation runtime compared to other commercial FastSPICE tools, and delivered results tightly correlated to silicon data. Based on these results, CustomSim is now used in verification production flows for all other CMOS logic-compatible embedded non-volatile memory IP at eMemory.

"As a world-class IP provider, we are developing next-generation non-volatile memory devices, such as Neobit, NeoFlash and NeoEE, in various CMOS process technologies and must extensively verify our designs targeting multiple foundry processes," said Rick Shen, president of eMemory Technology, Inc. "After evaluating several commercial circuit simulators, we have chosen to standardize on Synopsys' CustomSim HSIM engine, because its best-in-class performance and SPICE-level precision enabled us to thoroughly validate our technology innovation with unmatched turnaround time."

The Synopsys CustomSim solution unifies the best-in-class NanoSim®, HSIM® and XA circuit simulation technologies with added multicore processing capabilities in a single verification solution. CustomSim delivers superior verification performance and capacity for all classes of design, including custom digital, memory and analog/mixed-signal. The comprehensive offering includes advanced analysis options for native circuit checking, power, signal and MOS reliability analysis, and mixed-signal simulation. CustomSim simplifies usability with a common set of inputs, outputs, device models and debug environment.

"CustomSim's unique hierarchical simulation technology delivers the capacity, performance and accuracy customers need to verify circuit behavior with post-layout silicon effects," said Bijan Kiani, vice president of product marketing at Synopsys. "CustomSim has consistently enabled companies like eMemory to effectively verify and deliver robust IC products with increased confidence."

About Synopsys

Synopsys, Inc. (Nasdaq: SNPS) is a world leader in electronic design automation (EDA), supplying the global electronics market with the software, intellectual property (IP) and services used in semiconductor design, verification and manufacturing. Synopsys' comprehensive, integrated portfolio of implementation, verification, IP, manufacturing and field-programmable gate array (FPGA) solutions helps address the key challenges designers and manufacturers face today, such as power and yield management, system-to-silicon verification and time-to-results. These technology-leading solutions help give Synopsys customers a competitive edge in bringing the best products to market quickly while reducing costs and schedule risk. Synopsys is headquartered in Mountain View, California, and has more than 65 offices located throughout North America, Europe, Japan, Asia and India. Visit Synopsys online at https://www.synopsys.com.

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