Open-Silicon Integrates 50 DesignWare Interface and Analog IP Products with 100 Percent Silicon Success

High-Quality, Fully Verified IP and Longstanding Relationship Between the Two Companies Accelerates Time-to-Market for Open-Silicon's Customers

MOUNTAIN VIEW, Calif., July 7 /PRNewswire-FirstCall/ -- Synopsys, Inc. (Nasdaq: SNPS), a world leader in software and IP for semiconductor design, verification and manufacturing, today announced that Open-Silicon, a leading system-on-chip (SoC) design and semiconductor manufacturing company, has licensed and integrated 50 high-speed DesignWare® IP products into customers' chips with 100 percent first-pass silicon success. The IP products include complete solutions consisting of configurable digital controllers, PHYs which support leading process technologies from 180 to 28 nanometers, verification IP, as well as analog front ends and data converters. In addition to integrating many of Synopsys' silicon-proven interface and analog IP products, Open-Silicon has incorporated building block and infrastructure IP from Synopsys' DesignWare Library into hundreds of designs. By accessing a broad range of pre-verified IP solutions from a trusted vendor, Open-Silicon has been able to consistently reduce integration and verification risk and shorten development cycles for their complex designs in a wide range of applications such as home networking, enterprise network processors, wireless base-stations, storage controllers and test equipment.

"Our customers look to us to execute complex interfaces with first time silicon success, including physical design, package design, and test engineering," said Taher Madraswala, vice president of engineering at Open-Silicon. "Our 100 percent success rate when using DesignWare IP is a reflection of Open-Silicon's IP integration capability, our tight technical relationship with Synopsys' engineers, and the high quality of Synopsys' complete solutions of silicon-proven DesignWare IP."

As one of the initial members of the SynopsysIP OEM Partner Program, Open-Silicon has access to the entire Synopsys portfolio of interface and analog IP solutions. As companies increasingly turn to leading SoC design and semiconductor manufacturing vendors like Open-Silicon to help build their complex SoC designs, Synopsys is proactively partnering with these vendors to provide silicon-proven IP solutions, expert technical support and comprehensive documentation to mitigate project risks. Synopsys and Open-Silicon provide customers with high-quality IP and design services that help them bring their products to market faster and with more differentiated features.

"For more than 15 years, Synopsys has been providing SoC designers with a broad portfolio of high-quality, silicon-proven IP solutions consisting of digital controllers, PHYs and VIP that reduce integration risk and development cost and speed time-to-market for SoC designs," said John Koeter, vice president of marketing for the Solutions Group at Synopsys. "As one of the industry's top fabless ASIC companies, Open-Silicon is focused on customer service, low cost, design schedules and first-time correct silicon. Our OEM partnership with Open-Silicon enables a larger customer base to access pre-qualified DesignWare IP allowing them to reduce development costs and risk."

About DesignWare IP

Synopsys is a leading provider of high-quality, silicon-proven interface and analog IP solutions for system-on-chip designs. Synopsys' broad IP portfolio delivers complete connectivity IP solutions consisting of controllers, PHY and verification IP for widely used protocols such as USB, PCI Express, DDR, SATA, Ethernet, HDMI and MIPI IP including 3G DigRF, CSI-2 and D-PHY. The analog IP family includes Analog-to-Digital Converters, Digital-to-Analog Converters, Audio Codecs, Video Analog Front Ends, Touch Screen Controllers and more. In addition, Synopsys offers SystemC™ transaction-level models to build virtual platforms for rapid, pre-silicon development of software. With a robust IP development methodology, extensive investment in quality and comprehensive technical support, Synopsys enables designers to accelerate time-to-market and reduce integration risk. For more information on DesignWare IP, visit: http://www.synopsys.com/designware. Follow us on Twitter at http://twitter.com/designware_ip.

About Synopsys

Synopsys, Inc. (Nasdaq: SNPS) is a world leader in electronic design automation (EDA), supplying the global electronics market with the software, intellectual property (IP) and services used in semiconductor design, verification and manufacturing. Synopsys' comprehensive, integrated portfolio of implementation, verification, IP, manufacturing and field-programmable gate array (FPGA) solutions helps address the key challenges designers and manufacturers face today, such as power and yield management, system-to-silicon verification and time-to-results. These technology-leading solutions help give Synopsys customers a competitive edge in bringing the best products to market quickly while reducing costs and schedule risk. Synopsys is headquartered in Mountain View, California, and has more than 65 offices located throughout North America, Europe, Japan, Asia and India. Visit Synopsys online at https://www.synopsys.com/.

About Open-Silicon, Inc.

Open-Silicon, Inc. is a leading semiconductor company focused on SoC realization for traditional ASIC, develop-to-spec, and derivative ICs. Open-Silicon's OpenMODEL brings together Open-Silicon's engineering technology and high-quality manufacturing services with one of the broadest partner ecosystems for IC development, spanning IC design, open market IP integration, wafer fabrication, and assembly/test services. Open-Silicon received the Global Semiconductor Alliance (GSA) award for Most Respected Private Semiconductor Company in 2008 and 2009. For more information, visit Open-Silicon's website at www.open-silicon.com or call 408-240-5700.

Synopsys and DesignWare are registered trademarks of Synopsys, Inc. SystemC is a trademark of the Open SystemC Initiative and is used under license. Any other trademarks or registered trademarks mentioned in this release are the intellectual property of their respective owners.

Editorial Contact:

Sheryl Gulizia
Synopsys, Inc.
650-584-8635
sgulizia@synopsys.com

Stephen Brennan
MCA
650-968-8900 x114
brennan@mcapr.com

SOURCE Synopsys, Inc.