Freescale and Synopsys Announce Multi-year Strategic Collaboration Agreement to Increase Verification Productivity

Collaboration Builds on Years of Successful Deployment of Synopsys Verification Solution

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MOUNTAIN VIEW, Calif., Oct. 26 /PRNewswire-FirstCall/ -- Synopsys, Inc. (NASDAQ: SNPS), a world leader in software and IP for semiconductor design, verification and manufacturing, today announced the expansion of its verification collaboration with Freescale. The integration of complex hardware enabled by Moore's law, IP, and embedded software content in modern devices is causing a rapid increase in verification complexity. Freescale and Synopsys have broadened their ongoing collaboration to manage this growing complexity. In addition to verification performance, efficiency, and methodology, the expanded collaboration will focus on managing the ever-increasing cost of verification, which can encompass up to 75 percent of the total cost of product development.

"Functional verification comprises the largest portion of our total cost of product development and has a greater impact on our production schedules than any other single factor," said Dan Cronin, vice president of hardware R&D, Networking Solutions Division at Freescale Semiconductor, Inc. "In addition to enabling us to deploy a state-of-the-art environment, our extended relationship is expected to address various verification metrics, including the efficiency of compute farms and productivity of engineers, allowing us to more efficiently manage the exploding total cost of verification."

"The strategic collaboration announced today builds on the successful verification engagement between Freescale and Synopsys over many years," said Manoj Gandhi, senior vice president and general manager, Verification Group, Synopsys, Inc. "As one of the early adopters of SystemVerilog, Freescale played an important role in advancing the standard. Our collaboration is now extending beyond this standard. With verification becoming a larger part of overall product development cost, we are collaborating with Freescale to deliver solutions that help manage the total cost of verification."

About Synopsys

Synopsys, Inc. (NASDAQ: SNPS) is a world leader in electronic design automation (EDA), supplying the global electronics market with the software, intellectual property (IP) and services used in semiconductor design, verification and manufacturing. Synopsys' comprehensive, integrated portfolio of implementation, verification, IP, manufacturing and field-programmable gate array (FPGA) solutions helps address the key challenges designers and manufacturers face today, such as power and yield management, software-to-silicon verification and time-to-results. These technology-leading solutions help give Synopsys customers a competitive edge in bringing the best products to market quickly while reducing costs and schedule risk. Synopsys is headquartered in Mountain View, California, and has more than 65 offices located throughout North America, Europe, Japan, Asia and India. Visit Synopsys online at http://www.synopsys.com/.

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