Synopsys MVSIM Adopted for Low Power Verification of STw8500 Mobile SoC Platform

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Comprehensive Support for Low Power Techniques and High Level of Accuracy Significantly Improve Bug Detection

MOUNTAIN VIEW, Calif., July 1 /PRNewswire-FirstCall/ -- Synopsys, Inc. (NASDAQ: SNPS), a world leader in software and IP for semiconductor design, verification and manufacturing, today announced that ST-Ericsson has adopted Synopsys' MVSIM low power dynamic verification solution for its STw8500 system-on-chip (SoC) platform for the mobile phone market. ST-Ericsson selected MVSIM for its proven ability to comprehensively verify low power techniques, including standby and built-in automated low power assertions, which enable the early detection of bugs. The tool's extensive support for the IEEE 1801 [Unified Power Format (UPF)] power format, on which the STw8500 project team has standardized, was also a deciding factor.

"Mobile handset applications are driven by battery life, which necessitates the application of complex low power design techniques," said Francois Martin, director of the Wireless Multimedia Digital SoC's Business Unit at ST-Ericsson. "We chose MVSIM because of its ability to comprehensively verify the advanced low power techniques we employ for our wireless designs and detect bugs early in our design cycle."

The voltage-level aware simulation in MVSIM enables accurate verification of low power designs and improves bug detection. Its comprehensive set of built-in, automated low power assertions boost verification productivity. MVSIM, a core component of Synopsys' Eclypse™ Low Power Solution, is production-proven and works at the RTL and gate levels. MVSIM addresses complex low power verification challenges and enables customers to achieve their high-quality goals while meeting time-to-market constraints.

"The mobile phone market imposes some of the most stringent requirements on power and therefore demands the most advanced low power verification solution," said Swami Venkat senior director of functional verification marketing at Synopsys. "Synopsys continues to pioneer tools and methodologies to help address low power verification challenges, including the recent publication of the *Verification Methodology Manual for Low Power* (VMM-LP) book. ST-Ericsson's adoption of MVSIM confirms the tool's growing momentum in the mobile market segment."

About Synopsys

Synopsys, Inc. is a world leader in electronic design automation (EDA), supplying the global electronics market with the software, intellectual property (IP) and services used in semiconductor design and manufacturing. Synopsys' comprehensive, integrated portfolio of implementation, verification, IP, manufacturing and field-programmable gate array (FPGA) solutions helps address the key challenges designers and manufacturers face today, such as power and yield management, software-to-silicon verification and time-to- results. These technology-leading solutions help give Synopsys customers a competitive edge in bringing the best products to market quickly while reducing costs and schedule risk. Synopsys is headquartered in Mountain View, California, and has more than 65 offices located throughout North America, Europe, Japan, Asia and India. Visit Synopsys online at http://www.synopsys.com/.

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Editorial Contacts:
Sheryl Gulizia
Synopsys, Inc.
650-584-8635
sgulizia@synopsys.com

Stephen Brennan
MCA, Inc.
650-968-8900 x114
sbrennan@mcapr.com

SOURCE: Synopsys, Inc.

Web site: http://www.synopsys.com/