Synopsys IC Compiler with Zroute Technology Achieves Successful Tapeout for Infineon Automotive Microcontroller

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Near 100 Percent Redundant Via Rate Boosts Reliability to a New Level

MOUNTAIN VIEW, Calif., June 9 /PRNewswire-FirstCall/ -- Synopsys, Inc. (NASDAQ: SNPS), a world leader in software and IP for semiconductor design, verification and manufacturing, today announced that IC Compiler with Zroute technology drove silicon success for automotive microcontrollers of Infineon Technologies AG (FSE: IFX) (PinkSheets: IFNNY), a world leading automotive semiconductor provider. IC Compiler's Zroute provided a near 100 percent redundant via rate, enabling leading-edge device reliability and allowing Infineon to successfully tape out the lead product of its high-performance automotive 32-bit microcontroller platform in an advanced embedded Flash technology. In addition, by deploying a hierarchical design implementation flow together with intelligent Multi Corner Multi Mode (MCMM) optimization across different scenarios, IC Compiler achieved outstanding quality of results, meeting the high performance, area and power targets, specifically of the Infineon TriCore™ 32-bit microcontrollers.

"Being on the leading edge of automotive applications, Infineon is striving to supply intelligent chips that increase the energy efficiency and safety of vehicles while offering optimal quality and reliability," said Hartmut Hiller, senior director, Design Methodology and Implementation at Infineon Technologies. "As single vias are one of the major factors impacting reliability, we are always looking for design implementation tools that deliver the highest redundant via rate possible."

Zroute's unique architecture takes advantage of best-in-class routing technologies to meet the demands of companies like Infineon that are developing complex chips with high reliability targets to fulfill the ever increasing quality requirements of automotive tier1 and original equipment manufacturers (OEMs) all over the world. Utilizing concurrent design-for-manufacturability (DFM) optimization techniques, including soft rules and via redundancy, Zroute makes an efficient trade-off between manufacturability and the traditional design goals of timing, area, power and signal integrity. In addition, Zroute's native multicore support takes advantage of the latest microprocessor architectures to deliver near-linear scalability of runtimes as the number of cores increase.

"The selection of IC Compiler by Infineon and the recent complex automotive device tapeouts with Zroute are examples of Synopsys' IC Compiler technology driving customer success across the board," said Antun Domic, senior vice president and general manager of Synopsys' Implementation Group. "Synopsys values the relationship with Infineon and is committed to working closely with Infineon to evolve our product and methodologies based on feedback and requirements from the design teams of their leading-edge automotive applications."

About Synopsys

Synopsys, Inc. (NASDAQ: SNPS) is a world leader in electronic design automation (EDA), supplying the global electronics market with the software, intellectual property (IP) and services used in semiconductor design and manufacturing. Synopsys' comprehensive, integrated portfolio of implementation, verification, IP, manufacturing and field-programmable gate array (FPGA) solutions helps address the key challenges designers and manufacturers face today, such as power and yield management, software-to-silicon verification and time-to-results. These technology-leading solutions help give Synopsys customers a competitive edge in bringing the best products to market quickly while reducing costs and schedule risk. Synopsys is headquartered in Mountain View, California, and has more than 60 offices located throughout North America, Europe, Japan, Asia and India. Visit Synopsys online at http://www.synopsys.com/.

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