Synopsys PrimeTime PX Power Analysis Solution Achieves Broad Market Adoption

Highly-Accurate, Easy-to-Adopt Solution in Use by More Than 175 Semiconductor Companies Worldwide

MOUNTAIN VIEW, Calif., May 14 /PRNewswire-FirstCall/ -- Synopsys, Inc. (NASDAQ: SNPS), a world leader in software and IP for semiconductor design and manufacturing, today announced that Synopsys' PrimeTime® PX solution, a key component of the Galaxy™ Implementation Platform and part of Synopsys' Eclypse™ Low Power Solution, has been successfully deployed at more than 175 semiconductor companies worldwide to perform highly accurate dynamic and leakage power analysis. Seamless integration within PrimeTime, the golden industry standard for timing and signal integrity signoff, has resulted in the selection of PrimeTime PX as the preferred power analysis solution at companies from all facets of the semiconductor industry. These companies include Analog Devices, Aquantia, Atmel®, Fujitsu Microelectronics Limited, Integrated Device Technology, Realtek, Renesas Technology and STMicroelectronics.

"STMicroelectronics and Synopsys have collaborated on power modeling and analysis for over 15 years," said Indavong Vongsavady, CAD director, Technology R&D, STMicroelectronics. "Accurate understanding of power, especially static power, is fundamentally important given the rapidly increasing share of designs at 45/40 nanometers. PrimeTime PX represents a trusted solution, and is an integral part of our design flow from estimations early in the design process to sign-off, contributing to our record of first-time silicon successes. "

"Low power design is a crucial element for the Ten Gigabit Ethernet solution that we design," said Ramin Shirani, vice president of Engineering at Aquantia. "Being PrimeTime users, our engineers found the PrimeTime PX solution to be a natural fit within our existing design flow. We consistently use PrimeTime PX throughout our design flow to ensure that our ICs meet our stringent power targets."

"Our products have enabled key advances in power efficiency in various applications spanning communication network, computer peripheral and multimedia markets," said Chao-Cheng Lee, vice president at Realtek. "Synopsys' PrimeTime PX solution provides great visibility into IC power consumption throughout the design implementation process. Our design engineers trust the tool for power signoff because of its high accuracy."

Battery life, energy efficiency and product reliability are key concerns in complex system-on-chip (SoC) designs, and power analysis has become an integral part of today's design flows. PrimeTime PX expands the PrimeTime timing and signal integrity analysis solution to deliver highly accurate dynamic and leakage power analysis for designs at 90 nanometers (nm) and below. The integration of timing, signal integrity, and power eliminates redundant set-up and calculation steps required when using separate standalone tools. PrimeTime PX features a rich user interface familiar to PrimeTime users, as well as intuitive commands and reports designed to improve productivity. PrimeTime PX is able to complete power analysis on multi-million-gate designs within hours. The latest release has several new advanced features, including cycle-accurate peak power analysis (CAPP) to help designers determine the cycle during which maximum power consumption occurs in a design.

"Due to its ease of adoption and robust feature set, PrimeTime PX is quickly becoming the golden industry standard for gate-level power analysis and signoff at both the block and full-chip levels," said Bijan Kiani, vice president of Product Marketing at Synopsys. "Anchored by widely trusted tools including PrimeTime PX, the Synopsys Galaxy Implementation Platform enables broad adoption of advanced low power design techniques."

About Synopsys

Synopsys, Inc. (NASDAQ: SNPS) is the world leader in electronic design automation (EDA), supplying the global electronics market with the software, intellectual property (IP) and services used in semiconductor design and manufacturing. Synopsys' comprehensive, integrated portfolio of implementation, verification, IP, manufacturing and field-programmable gate array (FPGA) solutions helps address the key challenges designers and manufacturers face today, such as power and yield management, software-to-silicon verification and time-to-results. These technology-leading solutions help give Synopsys customers a competitive edge in bringing the best products to market quickly while reducing costs and schedule risk. Synopsys is headquartered in Mountain View, California, and has more than 60 offices located throughout North America, Europe, Japan, Asia and India. Visit Synopsys online at https://www.synopsys.com.

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Editorial Contacts:

Sheryl Gulizia
Synopsys, Inc.
650-584-8635
sgulizia@synopsys.com

Lisa Gillette-Martin
MCA, Inc.
650-968-8900 ext. 115
lgmartin@mcapr.com

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Web site: https://www.synopsys.com/