Toshiba Selects Synopsys as Its Key EDA Partner

Technology Leaders Deepen Collaboration to Focus on Reducing Design Cycle Time

MOUNTAIN VIEW, Calif., May 12 /PRNewswire-FirstCall/ **--** Synopsys, Inc. , a world leader in software and IP for semiconductor design and manufacturing, today announced that Toshiba Corporation has signed a multi-year business agreement to establish Synopsys as its key EDA partner across the Toshiba semiconductor design flow. As a result of the expanded relationship, Toshiba Corporation plans to expand use of Synopsys' verification and implementation design flow while partnering to reduce design cycle time.

"For longer than five years we have continued to build a successful relationship with Synopsys by collaborating across a broad set of next-generation EDA, commercial IP, and manufacturing technologies," said Tomotaka Saito, senior general manager of System LSI Division, Semiconductor Company, Toshiba Corporation. "The resulting products and design flow have enabled Toshiba to tape-out a number of chips across multiple technologies. Our existing Synopsys relationship helps solve current and future SoC design needs."

The latest agreement between the two companies expands Toshiba's use of solutions across Synopsys' comprehensive portfolio. These products include Synopsys' Galaxy™ design platform featuring IC Compiler place-and-route technology, Design Compiler® synthesis, DFT MAX test synthesis, TetraMAX® automatic test pattern generation, PrimeTime® timing analysis, Star-RCXT™ parasitic extraction, and IC Validator physical verification; Synopsys' Discovery™ verification platform featuring the VCS®, HSPICE® and CustomSim™ simulators for analog and digital verification; DesignWare® Library; the Synplicity® FPGA design solution; and the Eclypse™ Low Power Solution.

"Toshiba Corporation and Synopsys share many common goals, including pioneering high technology to develop products that create excitement," said Aart de Geus, chairman and CEO of Synopsys. "Synopsys greatly appreciates the long-standing collaboration with Toshiba that has resulted in numerous technology innovations deployed across consumer applications from digital TVs and mobile handsets to CMOS image sensors. By deepening our partnership, Synopsys intends to help advance Toshiba's technology strengths and increase R&D efficiency by reducing their design cycle time."

About Synopsys

Synopsys, Inc. is the world leader in electronic design automation (EDA), supplying the global electronics market with the software, intellectual property (IP) and services used in semiconductor design and manufacturing. Synopsys' comprehensive, integrated portfolio of implementation, verification, IP, manufacturing and field-programmable gate array (FPGA) solutions helps address the key challenges designers and manufacturers face today, such as power and yield management, software-to-silicon verification and time-to-results. These technology-leading solutions help give Synopsys customers a competitive edge in bringing the best products to market quickly while reducing costs and schedule risk. Synopsys is headquartered in Mountain View, California, and has more than 60 offices located throughout North America, Europe, Japan, Asia and India. Visit Synopsys online at https://www.synopsys.com/.

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