Priva Technologies Integrates Synopsys Galaxy Custom Designer Solution Into SoC Design Flow

Ease of Migration and Enhanced Features Key to Productivity

MOUNTAIN VIEW, Calif., April 13 /PRNewswire-FirstCall/ -- Synopsys, Inc. , a world leader in software and IP for semiconductor design and manufacturing, today announced that Priva Technologies, Inc. has adopted Synopsys' Galaxy Custom Designer™ custom implementation solution to design its next-generation security system-on-chip (SoC) integrated circuits (ICs). As a supplier of advanced authentication technologies and transaction processing solutions that integrate high-performance processing, biometric interfaces, tamper-resistant design, and wireless communication, Priva needed a modern-era analog/mixed-signal (AMS) design solution that could integrate easily into its SoC design flow and offer proven productivity enhancements.

"To increase productivity on our next-generation IC designs, we needed a modern AMS solution with the flexibility to easily integrate into our security-based SoC design flow," said Jeff Berkman, CTO for Priva Technologies. "Migrating our legacy data into Custom Designer was fast and accurate. The design team found using Custom Designer to be intuitive to use and quickly started taking advantage of the enhanced feature set."

Built from the ground up, Custom Designer was architected for productivity. Key modules include a schematic editor featuring on-canvas editing and dynamic net highlighting. The simulation environment provides a common use model allowing access to Synopsys simulators, including the HSPICE® and CustomSim™ circuit simulation solutions and the WaveView Analyzer tool. The layout editor features a real-time preview of P-cell parameter changes, and the results from Synopsys' Hercules™ DRC/LVS and Star-RCXT™ parasitic extraction tools are dynamically available within Custom Designer. In addition, Custom Designer enables complete data transparency with Synopsys' IC Compiler physical implementation solution, allowing the exchange of vital information during floorplanning, placement, routing and final chip editing to reduce time-consuming design iterations.

"We are working closely with companies like Priva to enable them to leverage Custom Designer's full range of capabilities to achieve higher performance and productivity, and to help them realize a competitive advantage," said Bijan Kiani, vice president of Product Marketing at Synopsys. "Priva's successful deployment of Custom Designer in their design flow underscores Synopsys' commitment to help designers address AMS design challenges with efficiency."

About Synopsys

Synopsys, Inc. is the world leader in electronic design automation (EDA), supplying the global electronics market with the software, intellectual property (IP) and services used in semiconductor design and manufacturing. Synopsys' comprehensive, integrated portfolio of implementation, verification, IP, manufacturing and field-programmable gate array (FPGA) solutions helps address the key challenges designers and manufacturers face today, such as power and yield management, software-to-silicon verification and time-to-results. These technology-leading solutions help give Synopsys customers a competitive edge in bringing the best products to market quickly while reducing costs and schedule risk. Synopsys is headquartered in Mountain View, California, and has more than 60 offices located throughout North America, Europe, Japan, Asia and India. Visit Synopsys online at https://www.synopsys.com/.

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