Synopsys Selected by Marvell as Its Primary EDA Partner

PRNewswire
MOUNTAIN VIEW, Calif.
(NASDAQ-NMS:SNPS)

Expanded Business Agreement Aimed at Increasing Design Efficiency

MOUNTAIN VIEW, Calif., April 7 /PRNewswire-FirstCall/ -- Synopsys, Inc. , a world leader in software and IP for semiconductor design and manufacturing, today announced that Marvell, a leader in the development of storage, communications and consumer silicon solutions, has signed an expanded business agreement to establish Synopsys as its primary EDA partner. Marvell chose Synopsys because of its broad product portfolio and its ability to help Marvell meet aggressive product schedules by consistently delivering a proven flow and providing outstanding global support.

"The foundation for Marvell's growth as a leader in today's highly competitive semiconductor industry is the development of breakthrough technology combined with dedication to customers and consistent delivery of high quality products," said Dr. Pantas Sutardja, chief technology officer at Marvell. "During the last three years, we have deployed a broad set of Synopsys technology solutions to tape-out multiple 90- and 65-nanometer production designs."

The latest agreement between the two companies expands Marvell's use of solutions across Synopsys' portfolio. These products include Synopsys' Galaxy design platform featuring IC Compiler place-and-route technology, Design Compiler® synthesis, DFTMax test synthesis, TetraMAX® automatic test pattern generation, PrimeTime® timing analysis, and Star-RCXT™ parasitic extraction; Synopsys' Discovery verification platform featuring the VCS®, HSPICE® and CustomSim simulators for analog and digital verification; DesignWare® library; the Synplicity® FPGA design solution; and System level design featuring Innovator virtual platform and DesignWare System-level libraries.

"In the current economic climate, leading semiconductor companies are focusing on improving operational efficiency worldwide and reinforcing their long-term financial strength," said Chi-Foon Chan, president and chief operating officer at Synopsys. "Synopsys values the relationship with Marvell that has resulted in several silicon solutions for mobile, desktop, enterprise and consumer markets. This collaboration is intended to increase Marvell's R&D productivity by using the Synopsys implementation and AMS verification solution that will help them meet aggressive design schedules."

About Synopsys

Synopsys, Inc. is the world leader in electronic design automation (EDA), supplying the global electronics market with the software, intellectual property (IP) and services used in semiconductor design and manufacturing. Synopsys' comprehensive, integrated portfolio of implementation, verification, IP, manufacturing and field-programmable gate array (FPGA) solutions helps address the key challenges designers and manufacturers face today, such as power and yield management, software-to-silicon verification and time-to-results. These technology-leading solutions help give Synopsys customers a competitive edge in bringing the best products to market quickly while reducing costs and schedule risk. Synopsys is headquartered in Mountain View, California, and has more than 60 offices located throughout North America, Europe, Japan, Asia and India. Visit Synopsys online at http://www.synopsys.com/.

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Editorial Contact:

Yvette Huygen
Synopsys, Inc.
650-584-4547
yvetteh@synopsys.com

Investor Contact:
Lisa Ewbank
Synopsys, Inc.
650-584-1901

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Web site: http://www.synopsys.com/