# Synopsys IC Compiler Zroute Wins EDN Innovation Award

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MOUNTAIN VIEW, Calif., April 2 /PRNewswire-FirstCall/ -- Synopsys, Inc. , a world leader in software and IP for semiconductor design and manufacturing, today announced that IC Compiler Zroute has been selected by *EDN* as the winner of this year's Innovation Award in the 'EDA: Design Creation and IP' category. Zroute is a new state-of-the-art full-chip router, built from the ground up to address forward-looking challenges in chip complexity and manufacturability. Held Monday in San Jose, California, *EDN's* 19th Annual Innovation Awards ceremony honored a diverse group of electronics engineers and the ground-breaking products they produce.

"Its on-average 10X routing speed-up, coupled with enhanced features to address manufacturability, have moved IC Compiler Zroute into broad usage within a few months of its initial production release," said Antun Domic, senior vice president and general manager of Synopsys' Implementation Group. "We continue to help solve our customers' most pressing issues in design productivity. This award from *EDN*, recognizing the value delivered by Zroute, demonstrates how Synopsys continues to lead through technical innovation."

Synopsys introduced Zroute into IC Compiler to address emerging design and design-for-manufacturing (DFM) challenges. Zroute offers three important differentiators: state-of-the-art routing technology, concurrent DFM optimizations, and near-linear multi-threading performance increase throughout. In addition to providing a boost in routing speed of 10X on average using mainstream quad-core compute platforms, Zroute provides superior via-optimizations, significantly enhanced lithography-friendly routing patterns, and overall faster design closure. Zroute has been available as a standard capability in IC Compiler since the 2008.09 release, offering an alternate choice for routing technology which can be enabled by customers as required. Zroute is in active production use world-wide, across a diverse set of applications.

The *EDN* Innovation program relies on a multitier process to choose finalists and winners. Working in panels organized by area of expertise, *EDN's* technical editors select finalists from the large number of deserving entries nominated each fall. After the list of finalists is published, *EDN's* worldwide audience of electronics engineers and engineering managers vote via online ballot. These reader votes are combined with the votes of *EDN's* editorial staff and consulting editor to determine the winners.

"*EDN* editors evaluated hundreds of submissions to determine the products we would present to our readers as finalists in 26 categories for *EDN's* 19th Annual Innovation Awards," said Rick Nelson, *EDN* editor-in-chief. "With its focus on multicore architectures and DFM problem solving, Zroute was a strong contender, and readers and editors voted it the winner in the 'EDA: Design Creation and IP' category."

**About Synopsys**

Synopsys, Inc. is a world leader in electronic design automation (EDA), supplying the global electronics market with the software, intellectual property (IP) and services used in semiconductor design and manufacturing. Synopsys' comprehensive, integrated portfolio of implementation, verification, IP, manufacturing and field-programmable gate array (FPGA) solutions helps address the key challenges designers and manufacturers face today, such as power and yield management, software-to-silicon verification and time-to-results. These technology-leading solutions help give Synopsys customers a competitive edge in bringing the best products to market quickly while reducing costs and schedule risk. Synopsys is headquartered in Mountain View, California, and has more than 60 offices located throughout North America, Europe, Japan, Asia and India. Visit Synopsys online at http://www.synopsys.com/.

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