LSI Corporation Selects Synopsys as Its Primary EDA Partner

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Semiconductor and EDA Leaders Collaborate to Accelerate Design Productivity

MOUNTAIN VIEW, Calif., Feb. 18 /PRNewswire-FirstCall/ -- Synopsys, Inc. , a world leader in software and IP for semiconductor design and manufacturing, today announced that LSI Corporation , a leading provider of silicon, systems and software solutions for the storage and networking markets, has signed an expanded business agreement to establish Synopsys as its primary EDA partner across the LSI Corporation design flow. As a result of the expanded relationship, LSI Corporation plans to consolidate on Synopsys design flow for designs at 40-nanometers (nm) and below.

"During the last two years, we have successfully deployed Synopsys design tools to tape out a number of 65- and 40-nanometer production chips for networking and storage customers," said Sudhakar Sabada, Senior VP / General Manager, Custom Silicon Division. "Looking at design needs for next-generation products, we chose Synopsys because of its strong product portfolio, advanced technology coverage, superior product quality, and outstanding global support."

With this latest agreement, LSI Corporation has chosen products across Synopsys' product portfolio, including Synopsys' Galaxy™ design platform featuring IC Compiler place-and-route technology, Design Compiler® synthesis, PrimeTime® timing analysis, Star-RCXT™ parasitic extraction, Synopsys' Discovery™ verification platform featuring the VCS®, HSPICE® and HSIM™ simulators for analog and digital verification; DesignWare® Library; and the Eclypse™ Low Power solution.

"At LSI, our success is built on delivering more than technology. Through strategic partnerships, a comprehensive silicon-to-systems approach, and trusted relationships with our customers and their businesses, we're helping drive the next generation of products," said Jeff Richardson, executive vice president and general manager, Semiconductor Solutions Group at LSI Corporation. "By extending our long-term relationship with Synopsys, we can take full advantage of Synopsys' technology solution and services to drive further efficiencies in the design process across our business."

"LSI Corporation and Synopsys share a number of common goals, including providing innovative technology solutions that enable products which seamlessly bring people, information and digital content together," said Chi-Foon Chan, president and chief operating officer at Synopsys. "This collaboration can increase engineering productivity for LSI Corporation, accelerating delivery of next-generation IC solutions while reducing costs and schedule risk."

About Synopsys

Synopsys, Inc. is the world leader in electronic design automation (EDA), supplying the global electronics market with the software, intellectual property (IP) and services used in semiconductor design and manufacturing. Synopsys' comprehensive, integrated portfolio of implementation, verification, IP, manufacturing and field-programmable gate array (FPGA) solutions helps address the key challenges designers and manufacturers face today, such as power and yield management, software-to-silicon verification and time-to-results. These technology-leading solutions help give Synopsys customers a competitive edge in bringing the best products to market quickly while reducing costs and schedule risk. Synopsys is headquartered in Mountain View, California, and has more than 60 offices located throughout North America, Europe, Japan, Asia and India. Visit Synopsys online at http://www.synopsys.com/.

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