Synopsys Unveils IP OEM Partner Program with Arrow Electronics, Global Unichip and Open-Silicon

Companies Select Synopsys DesignWare IP as Preferred Solution for SoC Designs

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MOUNTAIN VIEW, Calif., Nov. 20 /PRNewswire-FirstCall/ -- Synopsys, Inc. (NASDAQ: SNPS), a world leader in software and IP for semiconductor design and manufacturing, today announced the launch of its IP OEM partner program with leading fabless ASIC companies and supply channel partners. Arrow Electronics, Inc and Global Unichip Corp. (GUC) join Open-Silicon, an early adopter of the program, as the first companies to sign multi-year agreements standardizing on Synopsys DesignWare® IP for their system-on-chip (SoC) designs. Companies are increasingly turning to fabless ASIC vendors and supply channel partners to help build their complex SoC designs, especially at 65-nanometer (nm) process technologies and below. To address this trend, Synopsys is partnering with key companies to provide seamless access to high-quality DesignWare IP such as PCI Express, USB, DDR and SATA.

Synopsys' IP OEM partner program enables participating companies to have one central supplier for their IP needs. Synopsys' broad portfolio of silicon-proven IP solutions have been deployed in a wide range of applications and process technologies. By gaining easy access to this portfolio, our partners and their end-customers can speed time-to-market and reduce the risk for their SoC designs. Furthermore, the IP OEM program allows the partners to focus their engineering talent on their core differentiation and not on project-based IP procurement. The IP OEM partners will also gain access to Synopsys' next generation of connectivity IP as it becomes available.

The IP OEM partner program extends beyond the traditional customer/supplier relationship by providing the necessary resources and infrastructure to help the partners deliver first-rate support to their end customers and easily create successful designs. Specialized on-site, in-depth product and integration training led by Synopsys engineers enables the partners to have an unprecedented level of expertise, resulting in an enhanced end-user experience.

"Synopsys has created a unique business model that is instrumental in helping the supply channel efficiently deliver high-quality designs to our customers," said Chris Miller, director of the Custom Logic Solutions Group at Arrow Electronics. "Expanding our ecosystem to include Synopsys' leading portfolio of silicon-proven IP enables us and our end customers to get products to the market faster."

"As an established IP provider, Synopsys understands the need for a new type of business model to address the growing trends towards outsourcing," said Keh-Ching Huang, marketing director at GUC. "Synopsys' reputation for delivering high-quality IP that supports leading process technologies is in line with GUC's leadership as a fabless device integrator. GUC is committed to first-tier quality by partnering with first-tier suppliers such as Synopsys to ensure the IP fits seamlessly into the design flow for complicated designs."

"As the leading provider of open market ASIC solutions for multiple foundries, we have always been impressed with the quality of Synopsys' IP and their excellent technical support," said Scott Houghton, vice-president business development at Open-Silicon. "We've had a successful long-term relationship with Synopsys and think this step towards working together more closely will result in the industry's best IP integration experience and fastest design time to market."

"By partnering with companies such as Arrow Electronics, Global Unichip Corp. and Open-Silicon, Synopsys is demonstrating its commitment to the IP business and its ecosystem," said John Koeter, vice president of marketing for the solutions group at Synopsys. "This is truly a win-win situation not only for Synopsys and our partners, but also for their customers who benefit from high quality designs that help them bring differentiated products to the market faster and with less risk. We are looking forward to working with our partners to make this program a success."

Availability

For more information on Synopsys' IP OEM partners or to contact us for more information, visit: http://www.synopsys.com/dw/oempartners.php.

About DesignWare IP

Synopsys offers a broad portfolio of high-quality, silicon-proven digital, mixed-signal and verification IP for system-on-chip designs. As a leading provider of connectivity IP, Synopsys delivers the industry's most comprehensive solutions for widely

used protocols such as USB, PCI Express, SATA, Ethernet and DDR. In addition to connectivity IP, Synopsys offers SystemC transaction-level models to build virtual platforms for rapid, pre-silicon development of software. When combined with a robust IP development methodology, extensive investment in quality and comprehensive technical support, DesignWare IP enables designers to accelerate time-to-market and reduce integration risk. For more information on DesignWare IP, visit: http://www.synopsys.com/designware

About Synopsys

Synopsys, Inc. (NASDAQ: SNPS) is a world leader in electronic design automation (EDA), supplying the global electronics market with the software, intellectual property (IP) and services used in semiconductor design and manufacturing. Synopsys' comprehensive, integrated portfolio of implementation, verification, IP, manufacturing and field-programmable gate array (FPGA) solutions helps address the key challenges designers and manufacturers face today, such as power and yield management, system-to-silicon verification and time-to-results. These technology-leading solutions help give Synopsys customers a competitive edge in bringing the best products to market quickly while reducing costs and schedule risk. Synopsys is headquartered in Mountain View, California, and has more than 60 offices located throughout North America, Europe, Japan, Asia and India. Visit Synopsys online at http://www.synopsys.com/.

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